

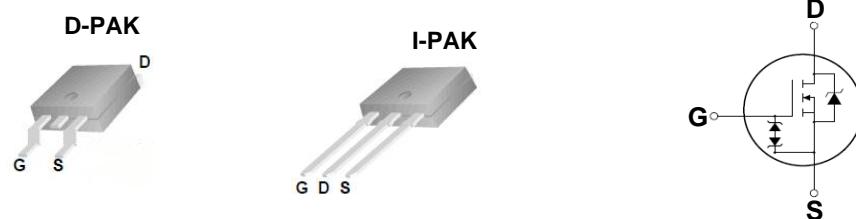
## Features

- Low gate charge
- 100% avalanche tested
- Improved dv/dt capability
- RoHS compliant
- Halogen free package
- JEDEC Qualification
- Improved ESD performance

$V_{DSS} = 440 \text{ V} @ T_{jmax}$

$I_D = 2\text{A}$

$R_{DS(on)} = 3.4 \Omega(\text{max}) @ V_{GS}= 10 \text{ V}$



Device	Package	Marking	Remark
TMD3N40ZG/TMU3N40ZG	D-PAK/I-PAK	TMD3N40ZG/TMU3N40ZG	Halogen Free

## Absolute Maximum Ratings

Parameter	Symbol	TMD3N40ZG/TMU3N40ZG	Unit
Drain-Source Voltage	$V_{DS}$	400	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current  $T_C = 25 \text{ }^\circ\text{C}$	$I_D$	2.0*	A
$T_C = 100 \text{ }^\circ\text{C}$		1.2*	A
Pulsed Drain Current (Note 1)	$I_{DM}$	8*	A
Single Pulse Avalanche Energy (Note 2)	$E_{AS}$	46	mJ
Repetitive Avalanche Current (Note 1)	$I_{AR}$	2	A
Repetitive Avalanche Energy (Note 1)	$E_{AR}$	3	mJ
Power Dissipation  $T_C = 25 \text{ }^\circ\text{C}$	$P_D$	30	W
Derate above 25 °C		0.24	W/°C
Peak Diode Recovery dv/dt (Note 3)	dv/dt	4.5	V/ns
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	$T_L$	300	°C

\* Limited only by maximum junction temperature

## Thermal Characteristics

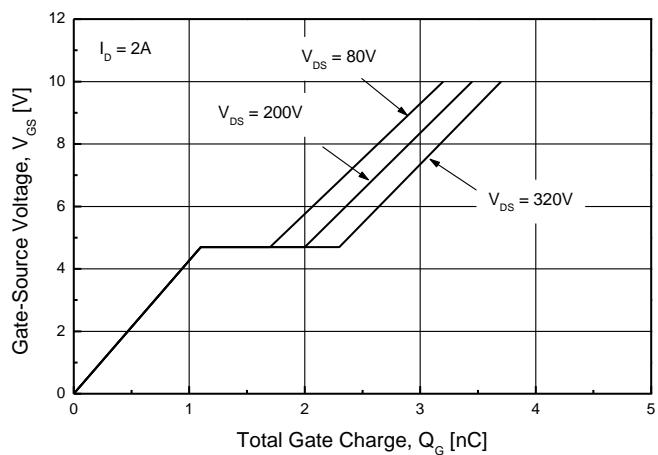
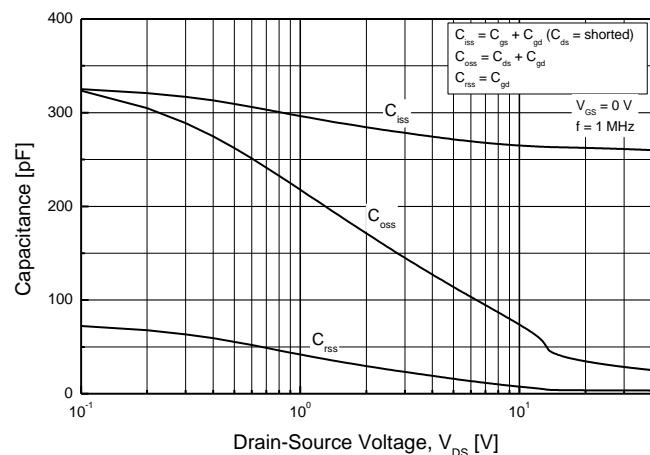
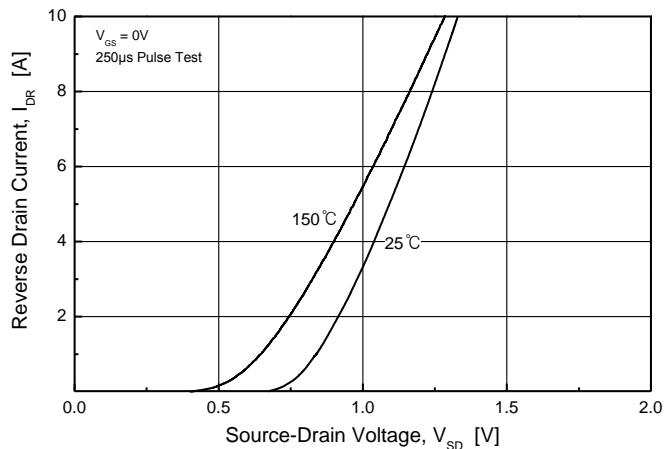
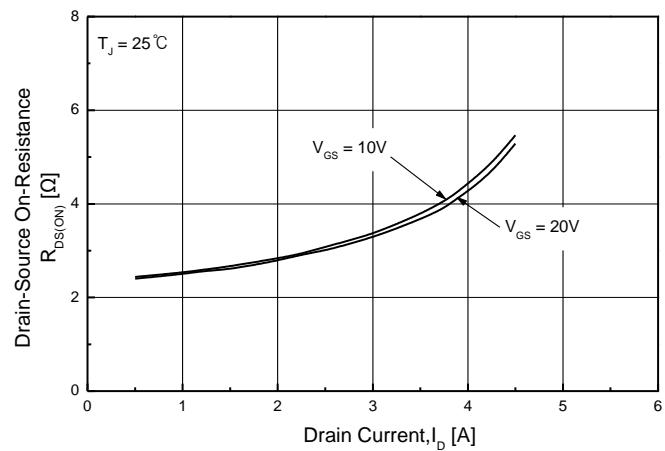
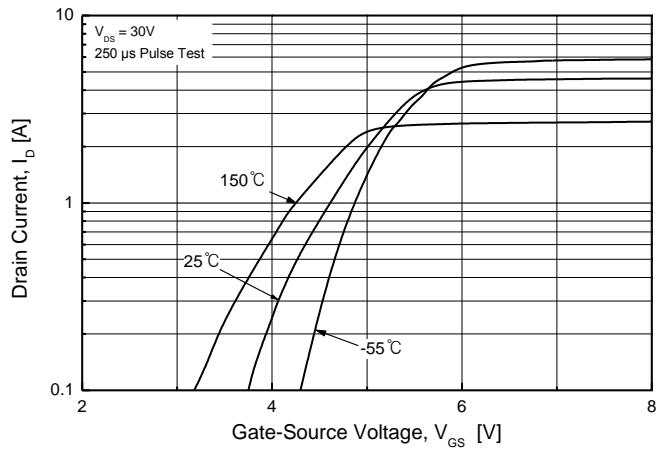
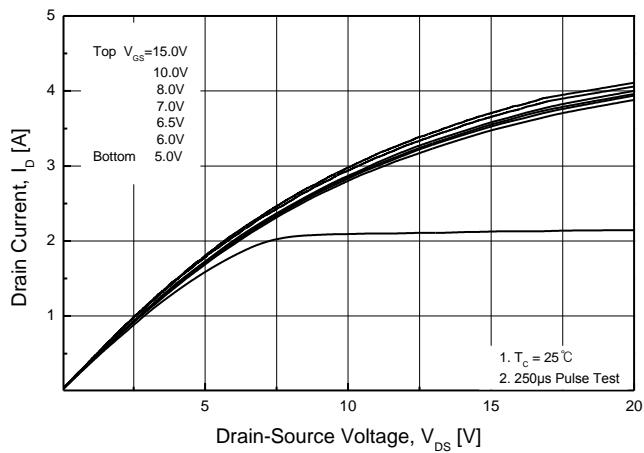
Parameter	Symbol	TMD3N40ZG/TMU3N40ZG	Unit
Maximum Thermal resistance, Junction-to-Case	$R_{\theta JC}$	4.2	°C/W
Maximum Thermal resistance, Junction-to-Ambient	$R_{\theta JA}$	110	°C/W

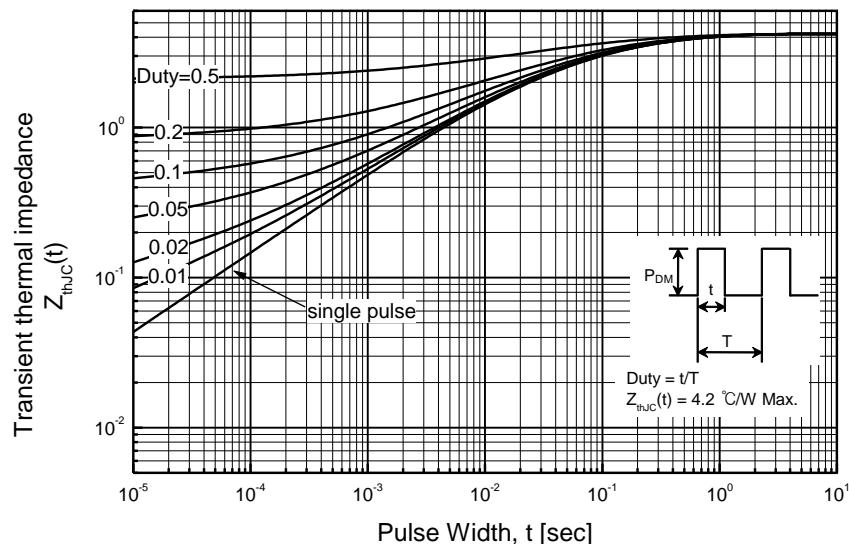
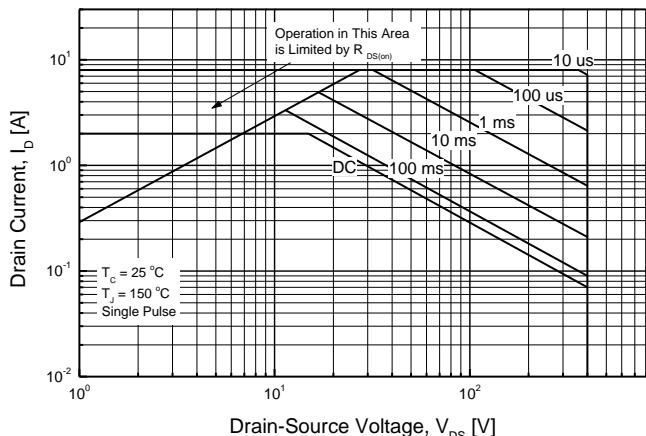
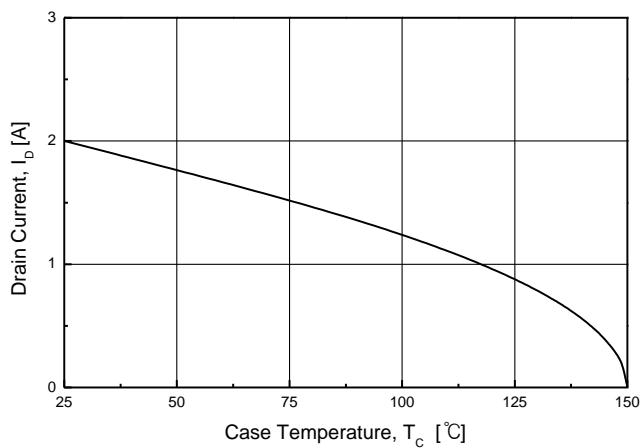
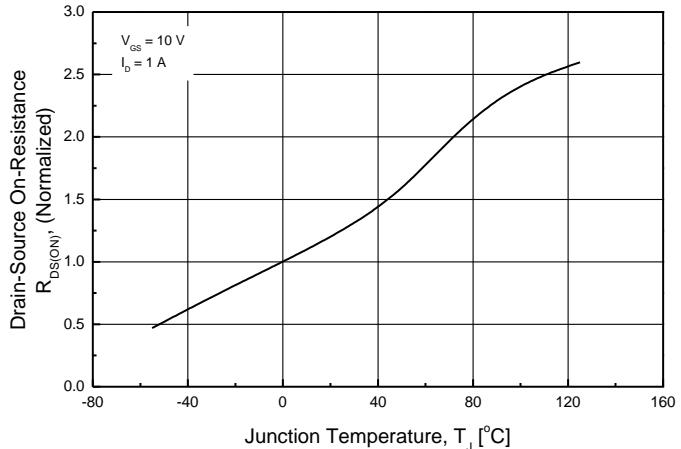
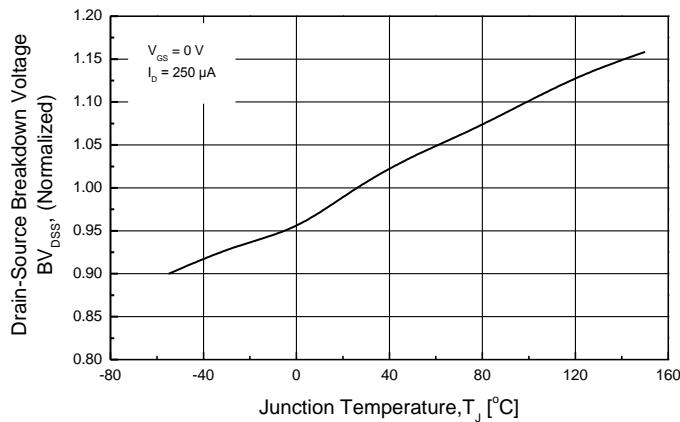
**Electrical Characteristics :  $T_c=25^\circ\text{C}$ , unless otherwise noted**

Parameter	Symbol	Test condition	Min	Typ	Max	Units
<b>OFF</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}, I_{\text{D}} = 250 \mu\text{A}$	400	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DS}}^{\text{SS}}$	$V_{\text{DS}} = 400 \text{ V}, V_{\text{GS}} = 0 \text{ V}$	--	--	1	$\mu\text{A}$
		$V_{\text{DS}} = 320 \text{ V}, T_c = 125^\circ\text{C}$	--	--	10	$\mu\text{A}$
Forward Gate-Source Leakage Current	$I_{\text{GSSF}}$	$V_{\text{GS}} = 30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	--	--	100	$\mu\text{A}$
Reverse Gate-Source Leakage Current	$I_{\text{GSSR}}$	$V_{\text{GS}} = -30 \text{ V}, V_{\text{DS}} = 0 \text{ V}$	--	--	-100	$\mu\text{A}$
<b>ON</b>						
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{DS}} = V_{\text{GS}}, I_{\text{D}} = 250 \mu\text{A}$	2	--	4	V
Drain-Source On-Resistance	$R_{\text{DS(on)}}$	$V_{\text{GS}} = 10 \text{ V}, I_{\text{D}} = 1 \text{ A}$	--	2.75	3.4	$\Omega$
Forward Transconductance <sup>(Note 4)</sup>	$g_{\text{FS}}$	$V_{\text{DS}} = 30 \text{ V}, I_{\text{D}} = 1 \text{ A}$	--	9	--	S
<b>DYNAMIC</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 25 \text{ V}, V_{\text{GS}} = 0 \text{ V}, f = 1.0 \text{ MHz}$	--	210	--	pF
Output Capacitance	$C_{\text{oss}}$		--	32	--	pF
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	3.7	--	pF
<b>SWITCHING</b>						
Turn-On Delay Time <sup>(Note 4,5)</sup>	$t_{\text{d(on)}}$	$V_{\text{DD}} = 200 \text{ V}, I_{\text{D}} = 2 \text{ A}, R_{\text{G}} = 25 \Omega$	--	8	--	ns
Turn-On Rise Time <sup>(Note 4,5)</sup>	$t_r$		--	9	--	ns
Turn-Off Delay Time <sup>(Note 4,5)</sup>	$t_{\text{d(off)}}$		--	19	--	ns
Turn-Off Fall Time <sup>(Note 4,5)</sup>	$t_f$		--	8	--	ns
Total Gate Charge <sup>(Note 4,5)</sup>	$Q_g$	$V_{\text{DS}} = 320 \text{ V}, I_{\text{D}} = 2 \text{ A}, V_{\text{GS}} = 10 \text{ V}$	--	3.7	--	nC
Gate-Source Charge <sup>(Note 4,5)</sup>	$Q_{\text{gs}}$		--	1.2	--	nC
Gate-Drain Charge <sup>(Note 4,5)</sup>	$Q_{\text{gd}}$		--	1.1	--	nC
<b>SOURCE DRAIN DIODE</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_s$	---	--	--	2	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{\text{SM}}$	---	--	--	8	A
Drain-Source Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}} = 0 \text{ V}, I_s = 2 \text{ A}$	--	--	1.5	V
Reverse Recovery Time <sup>(Note 4)</sup>	$t_{\text{rr}}$	$V_{\text{GS}} = 0 \text{ V}, I_s = 2 \text{ A}$ $dI_F / dt = 100 \text{ A}/\mu\text{s}$	--	174	--	ns
Reverse Recovery Charge <sup>(Note 4)</sup>	$Q_{\text{rr}}$		--	0.54	--	$\mu\text{C}$

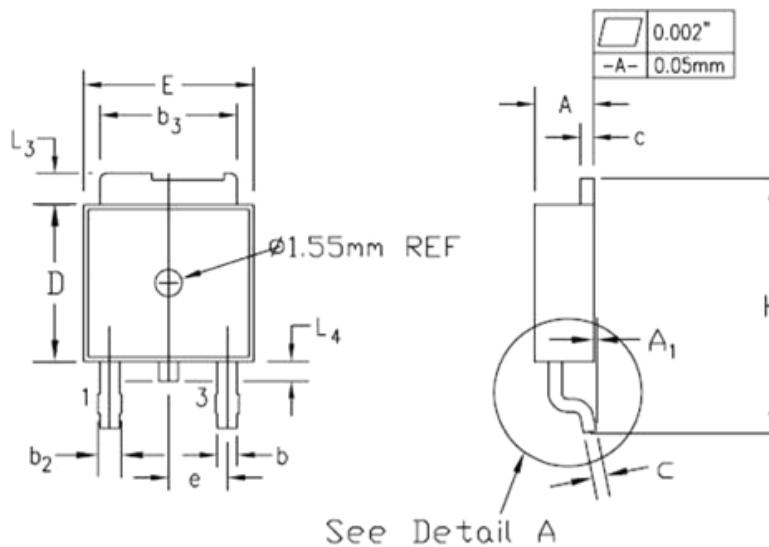
**Note :**

1. Repeated rating : Pulse width limited by safe operating area
2.  $L=10\text{mH}, I_{\text{AS}}=2\text{A}, V_{\text{DD}}=50\text{V}, R_{\text{G}}=25\Omega$ , Starting  $T_J=25^\circ\text{C}$
- 3  $I_{\text{SD}} \leq 2\text{A}, di/dt \leq 200\text{A}/\mu\text{s} , V_{\text{DD}} \leq \text{BV}_{\text{DS}}$ , Starting  $T_J=25^\circ\text{C}$
4. Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$
5. Essentially Independent of Operating Temperature Typical Characteristics

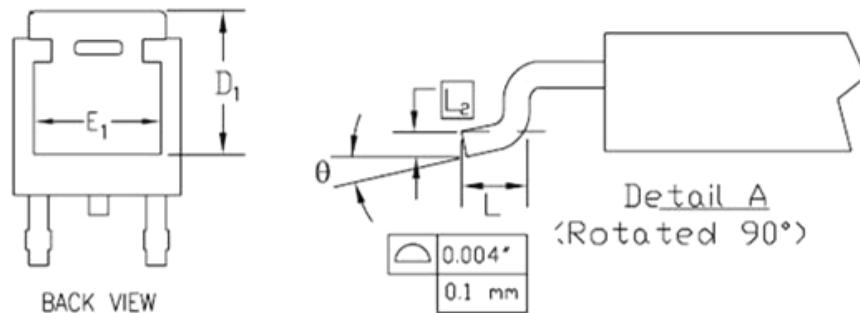




## TO-252 (D-PAK) MECHANICAL DATA

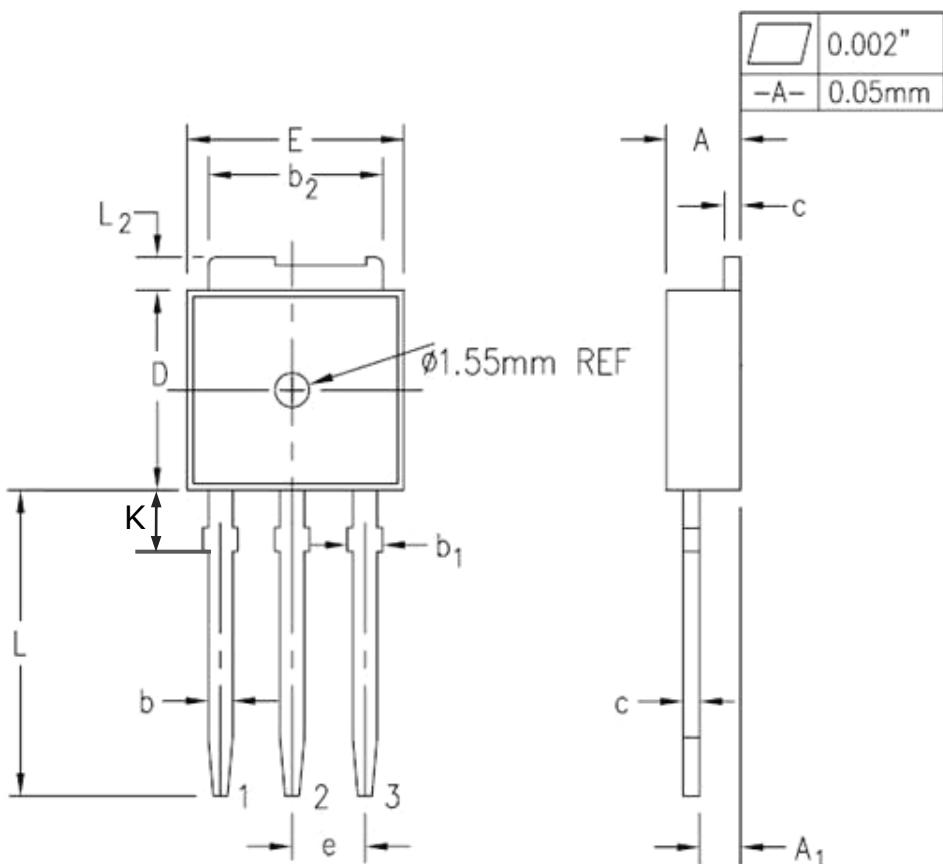


See Detail A



SYMBOL	INCHES		MILLIMETERS		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	0.086	0.094	2.19	2.38	
A <sub>1</sub>	—	0.005	—	0.13	
b	0.025	0.035	0.64	0.89	
b <sub>2</sub>	0.033	0.045	0.84	1.14	
b <sub>3</sub>	0.205	0.215	5.21	5.46	
c	0.018	0.024	0.46	0.61	
D	0.235	0.245	5.97	6.22	
D <sub>1</sub>	0.205	—	5.21	—	2
E	0.250	0.265	6.35	6.73	
E <sub>1</sub>	0.190	—	4.83	—	2
e	0.090	BSC	2.29	BSC	
H	0.380	0.410	9.65	10.41	
L	0.055	0.070	1.40	1.78	4
L <sub>2</sub>	0.020	BSC	0.51	BSC	
L <sub>3</sub>	0.035	0.050	0.89	1.27	
L <sub>4</sub>	0.025	0.040	0.64	1.01	3
θ	0°	8°	0°	8°	

## TO-251 (I-PAK) MECHANICAL DATA



SYMBOL	INCHES		MILIMETERS		NOTES
	MIN.	MAX.	MIN.	MAX.	
A	0.086	0.094	2.19	2.39	
A <sub>1</sub>	0.040	0.045	1.02	1.14	
b	0.025	0.035	0.64	0.89	
b <sub>1</sub>	0.037	0.045	0.95	1.14	
b <sub>2</sub>	0.205	0.215	5.21	5.46	
c	0.018	0.023	0.46	0.58	
D	0.235	0.245	5.97	6.22	
E	0.250	0.265	6.35	6.73	
e	0.090 TYP.		2.28 TYP.		
L	0.350	0.380	8.89	9.65	
L <sub>2</sub>	0.035	0.050	0.89	1.27	
K	0.079	0.096	2.00	2.44	