

## General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent stability and uniformity



## Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

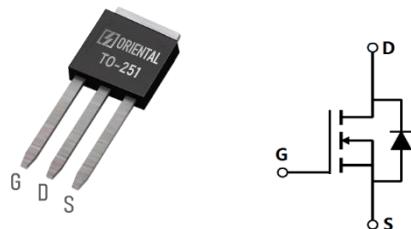
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS, min} @ T_j(max)$	850	V
$I_D, pulse$	12	A
$R_{DS(ON)}, max @ V_{GS}=10V$	1.4	$\Omega$
$Q_g$	7.5	nC

## Marking Information

Product Name	Package	Marking
OSG80R1K4AF	TO251	OSG80R1K4A

## Package & Pin Information



**Absolute Maximum Ratings** at  $T_j=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	800	V
Gate-source voltage	$V_{GS}$	$\pm 30$	V
Continuous drain current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_D$	4	A
Continuous drain current <sup>1)</sup> , $T_C=100^\circ\text{C}$		2.5	
Pulsed drain current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{D,\text{pulse}}$	12	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_S$	4	A
Diode pulsed current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{S,\text{pulse}}$	12	A
Power dissipation <sup>3)</sup> , $T_C=25^\circ\text{C}$	$P_D$	37	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	100	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\ldots 640\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\ldots 640\text{ V}$ , $I_{SD} \leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	3.4	°C/W
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62	°C/W

**Electrical Characteristics** at  $T_j=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	800			V	$V_{GS}=0\text{ V}$ , $I_D=250\text{ }\mu\text{A}$
		850	930			$V_{GS}=0\text{ V}$ , $I_D=250\text{ }\mu\text{A}$ , $T_j=150^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.0		4.0	V	$V_{DS}=V_{GS}$ , $I_D=250\text{ }\mu\text{A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		1.1	1.4	$\Omega$	$V_{GS}=10\text{ V}$ , $I_D=2\text{ A}$
			2.75			$V_{GS}=10\text{ V}$ , $I_D=2\text{ A}$ , $T_j=150^\circ\text{C}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=800\text{ V}$ , $V_{GS}=0\text{ V}$
Gate resistance	$R_G$			4.1	$\Omega$	F=1 MHz, Open drain

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C <sub>iss</sub>		363.5		pF	V <sub>GS</sub> =0 V, V <sub>DS</sub> =50 V, f=1 MHz
Output capacitance	C <sub>oss</sub>		25.5		pF	
Reverse transfer capacitance	C <sub>rss</sub>		1.35		pF	
Turn-on delay time	t <sub>d(on)</sub>		16.8		ns	V <sub>GS</sub> =10 V, V <sub>DS</sub> =400 V, R <sub>G</sub> =25 Ω, I <sub>D</sub> =2 A
Rise time	t <sub>r</sub>		5.7		ns	
Turn-off delay time	t <sub>d(off)</sub>		31		ns	
Fall time	t <sub>f</sub>		42.8		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q <sub>g</sub>		7.5		nC	V <sub>GS</sub> =10 V, V <sub>DS</sub> =400 V, I <sub>D</sub> =4 A
Gate-source charge	Q <sub>gs</sub>		2.1		nC	
Gate-drain charge	Q <sub>gd</sub>		2.9		nC	
Gate plateau voltage	V <sub>plateau</sub>		5.6		V	

### Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V <sub>SD</sub>			1.3	V	I <sub>S</sub> =4 A, V <sub>GS</sub> =0 V
Reverse recovery time	t <sub>rr</sub>		214.4		ns	I <sub>S</sub> =4 A, di/dt=100 A/μs
Reverse recovery charge	Q <sub>rr</sub>		1.6		μC	
Peak reverse recovery current	I <sub>rrm</sub>		13.3		A	

### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R<sub>θJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.
- 5) V<sub>DD</sub>=50 V, V<sub>GS</sub>=10 V, L=10 mH, starting T<sub>j</sub>=25 °C.

### Electrical Characteristics Diagrams

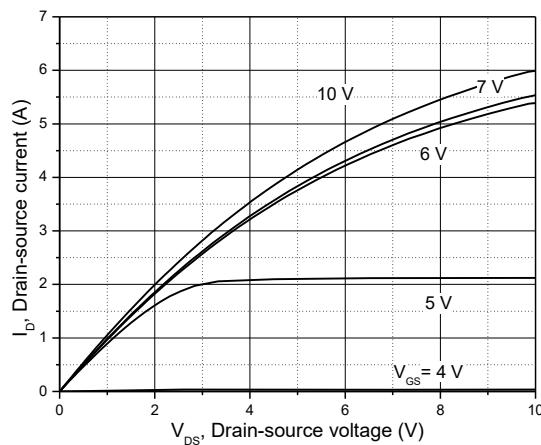


Figure 1. Typ. output characteristics

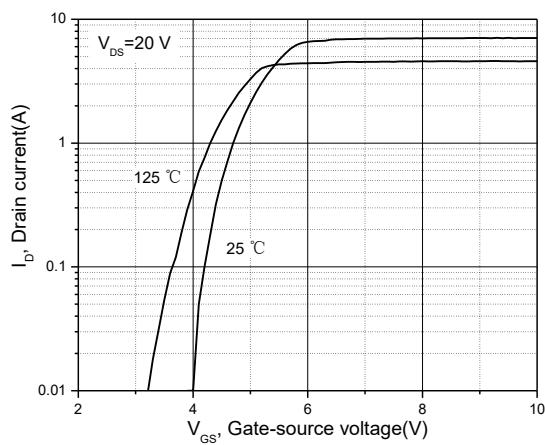


Figure 2. Typ. transfer characteristics

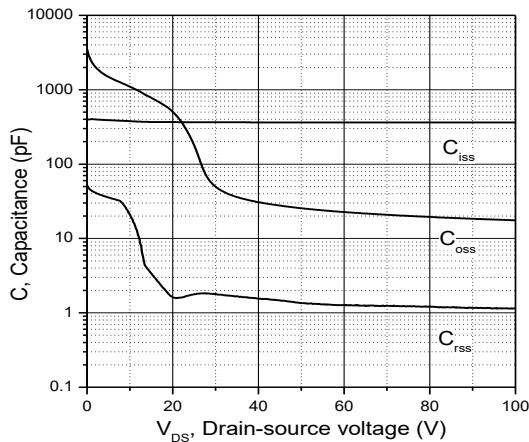


Figure 3. Typ. capacitances

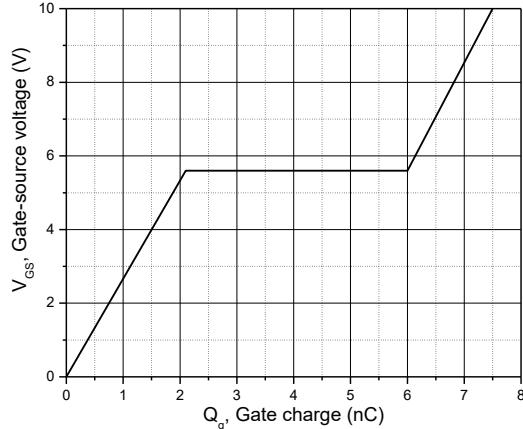


Figure 4. Typ. gate charge

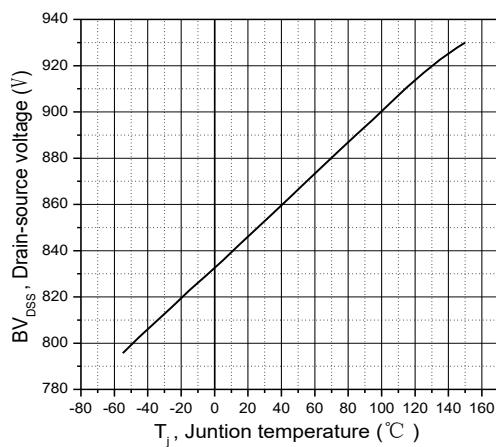


Figure 5. Drain-source breakdown voltage

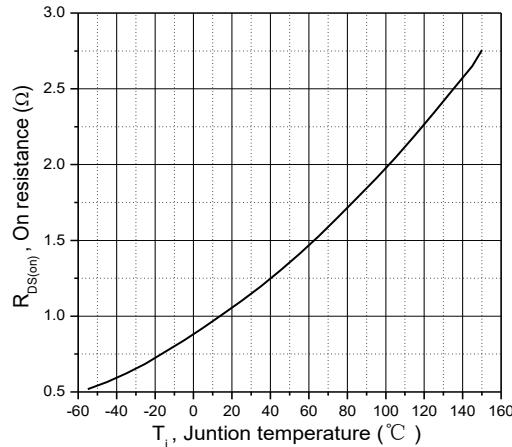
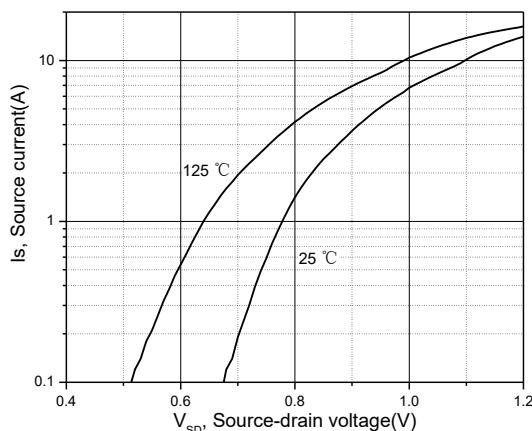
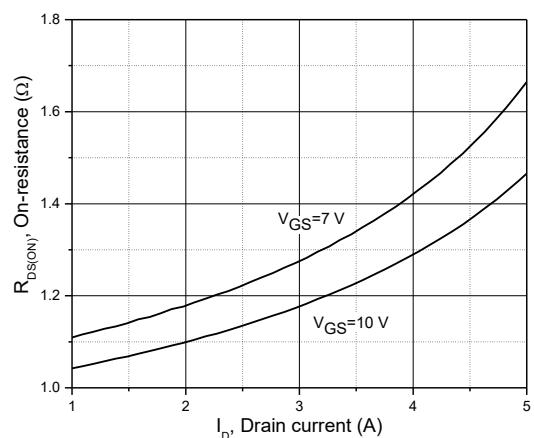


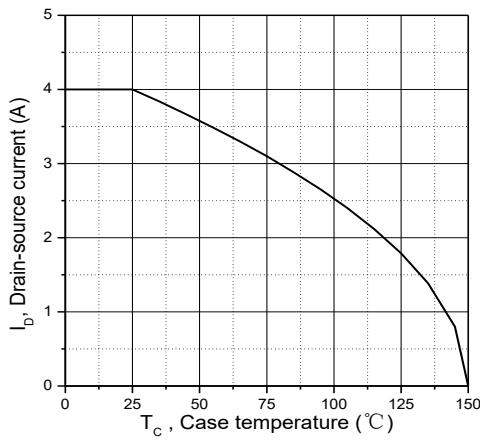
Figure 6. Drain-source on-state resistance



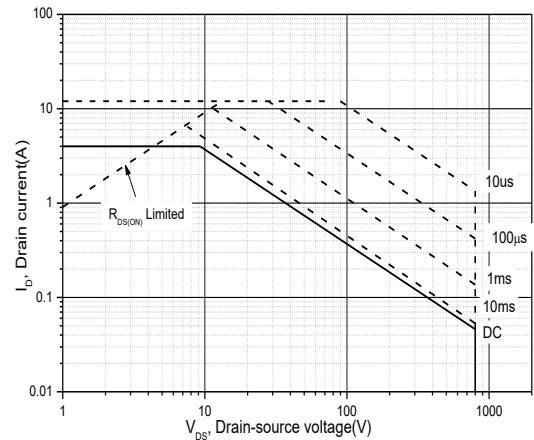
**Figure 7. Forward characteristic of body diode**



**Figure 8. Drain-source on-state resistance**



**Figure 9. Drain current**

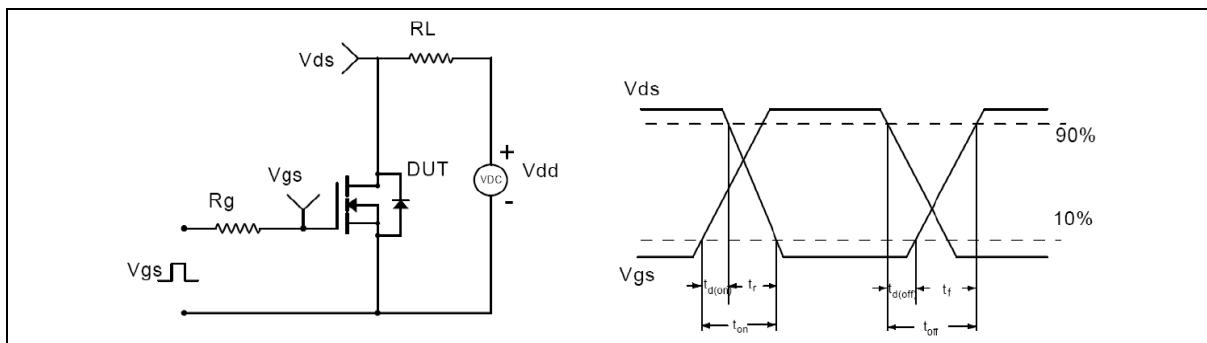


**Figure 10. Safe operation area  $T_c=25\text{ °C}$**

### Test circuits and waveforms



**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

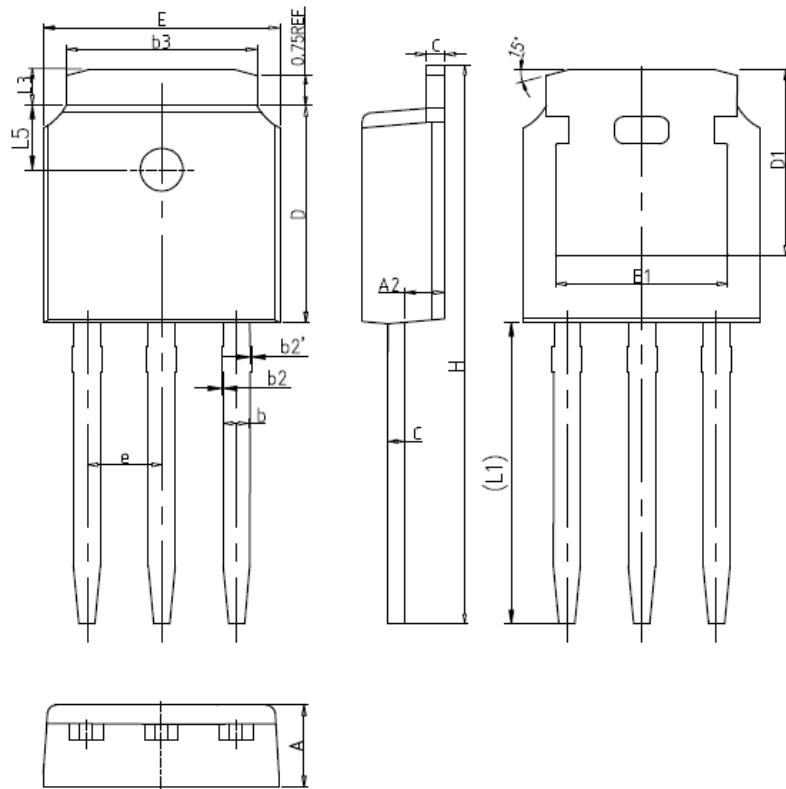


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

## Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.40
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b2	0.00	0.04	0.10
b2'	0.00	0.04	0.10
b3	5.20	5.33	5.50
c	0.43	0.53	0.63
D	5.98	6.10	6.22
D1	5.30REF		
E	6.40	6.60	6.80
E1	4.63	-	-
e	2.286BSC		
H	16.22	16.52	16.82
L1	9.15	9.40	9.65
L3	0.88	1.02	1.28
L5	1.65	1.80	1.95

Version 1: TO251-C package outline dimension

## Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO251-C	75	66	4950	6	29700

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG80R1K4AF	TO251	yes	yes	yes

## Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives ([www.orientalsemi.com](http://www.orientalsemi.com)).

© Oriental Semiconductor Co.,Ltd. All Rights Reserved

