

## General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® E series is optimized for its switching characteristics to achieve balance between EMI and efficiency. It is designed to enable power supply systems to reach the highest efficiency while still meeting EMI standards.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- EMI and performance balanced



## Applications

- LED lighting
- Charger
- Adapter
- TV power
- Telecom power
- Server power
- Solar/UPS

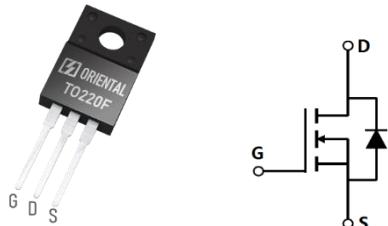
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS}$ , min @ $T_{j(max)}$	700	V
$I_D$ , pulse	15	A
$R_{DS(ON)}$ , max @ $V_{GS}=10V$	900	mΩ
$Q_g$	9.0	nC

## Marking Information

Product Name	Package	Marking
OSG65R900FEF	TO220F	OSG65R900FE

## Package & Pin Information



**Absolute Maximum Ratings** at  $T_j=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	650	V
Gate-source voltage	$V_{GS}$	$\pm 30$	V
Continuous drain current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_D$	5	A
Continuous drain current <sup>1)</sup> , $T_C=100^\circ\text{C}$		3.2	
Pulsed drain current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{D,\text{pulse}}$	15	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_S$	5	A
Diode pulsed current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{S,\text{pulse}}$	15	A
Power dissipation <sup>3)</sup> , $T_C=25^\circ\text{C}$	$P_D$	26	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	130	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\ldots 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\ldots 480\text{ V}$ , $I_{SD} \leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	4.8	°C/W
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62.5	°C/W

**Electrical Characteristics** at  $T_j=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	650			V	$V_{GS}=0\text{ V}$ , $I_D=250\text{ }\mu\text{A}$
		700	770			$V_{GS}=0\text{ V}$ , $I_D=250\text{ }\mu\text{A}$ , $T_j=150^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.9		3.9	V	$V_{DS}=V_{GS}$ , $I_D=250\text{ }\mu\text{A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		0.8	0.9	$\Omega$	$V_{GS}=10\text{ V}$ , $I_D=2.5\text{ A}$
			2.3			$V_{GS}=10\text{ V}$ , $I_D=2.5\text{ A}$ , $T_j=150^\circ\text{C}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=650\text{ V}$ , $V_{GS}=0\text{ V}$

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C <sub>iss</sub>		408.0		pF	V <sub>GS</sub> =0 V, V <sub>DS</sub> =50 V, f=1 MHz
Output capacitance	C <sub>oss</sub>		30.3		pF	
Reverse transfer capacitance	C <sub>rss</sub>		1.4		pF	
Turn-on delay time	t <sub>d(on)</sub>		27.4		ns	V <sub>GS</sub> =10 V, V <sub>DS</sub> =400 V, R <sub>G</sub> =2 Ω, I <sub>D</sub> =2.5 A
Rise time	t <sub>r</sub>		12.6		ns	
Turn-off delay time	t <sub>d(off)</sub>		52.2		ns	
Fall time	t <sub>f</sub>		7.6		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q <sub>g</sub>		9.0		nC	V <sub>GS</sub> =10 V, V <sub>DS</sub> =400 V, I <sub>D</sub> =2.5 A
Gate-source charge	Q <sub>gs</sub>		2.0		nC	
Gate-drain charge	Q <sub>gd</sub>		3.8		nC	
Gate plateau voltage	V <sub>plateau</sub>		5.6		V	

### Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V <sub>SD</sub>			1.3	V	I <sub>S</sub> =5 A, V <sub>GS</sub> =0 V
Reverse recovery time	t <sub>rr</sub>		146.6		ns	V <sub>R</sub> =400 V, I <sub>S</sub> =2.5 A, di/dt=100 A/μs
Reverse recovery charge	Q <sub>rr</sub>		1.1		μC	
Peak reverse recovery current	I <sub>rrm</sub>		15.5		A	

### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R<sub>θJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.
- 5) V<sub>DD</sub>=50 V, V<sub>GS</sub>=10 V, L=20 mH, starting T<sub>j</sub>=25 °C.

### Electrical Characteristics Diagrams

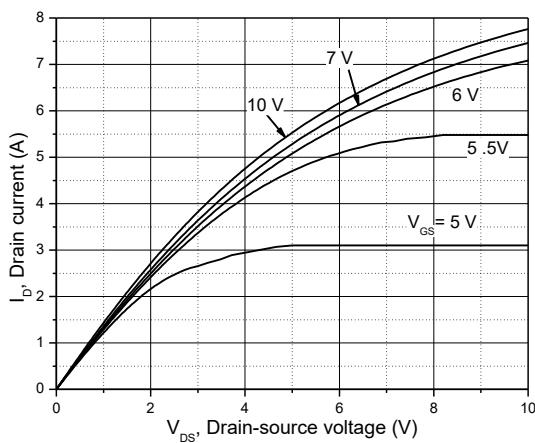


Figure 1. Typ. output characteristics

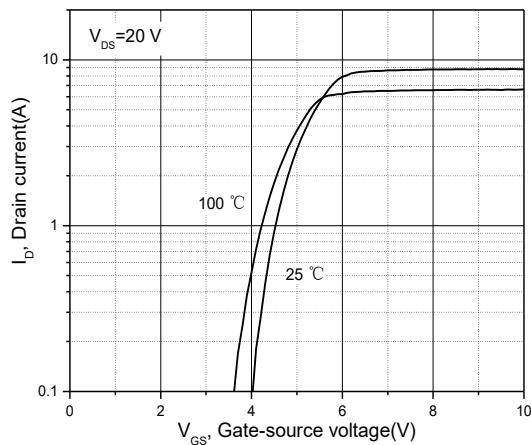


Figure 2. Typ. transfer characteristics

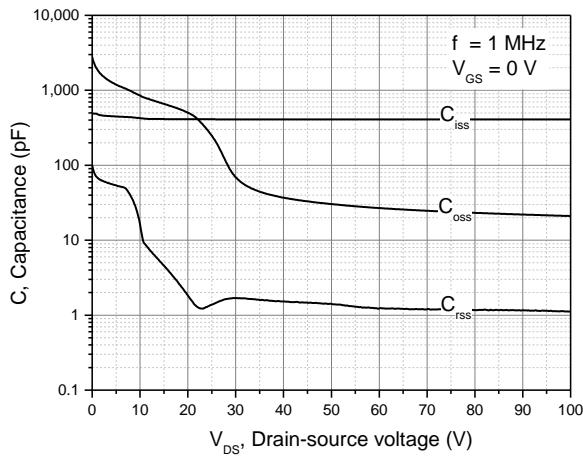


Figure 3. Typ. capacitances

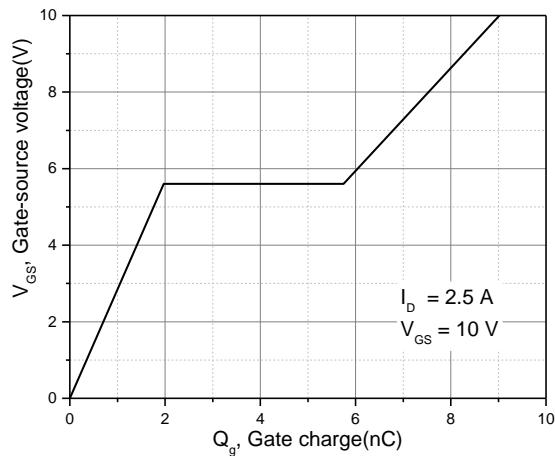


Figure 4. Typ. gate charge

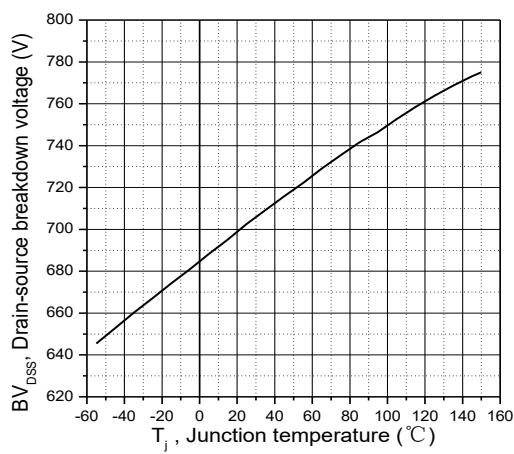


Figure 5. Drain-source breakdown voltage

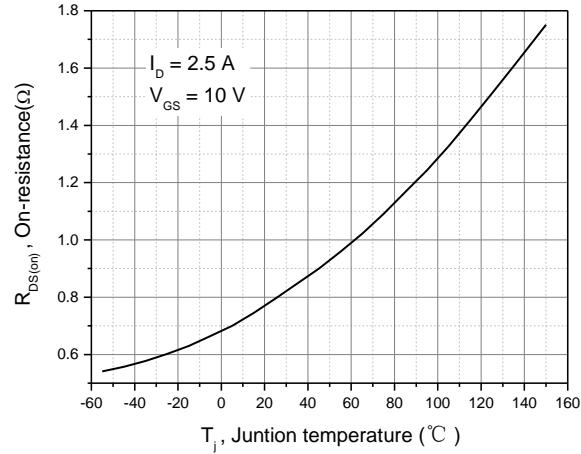
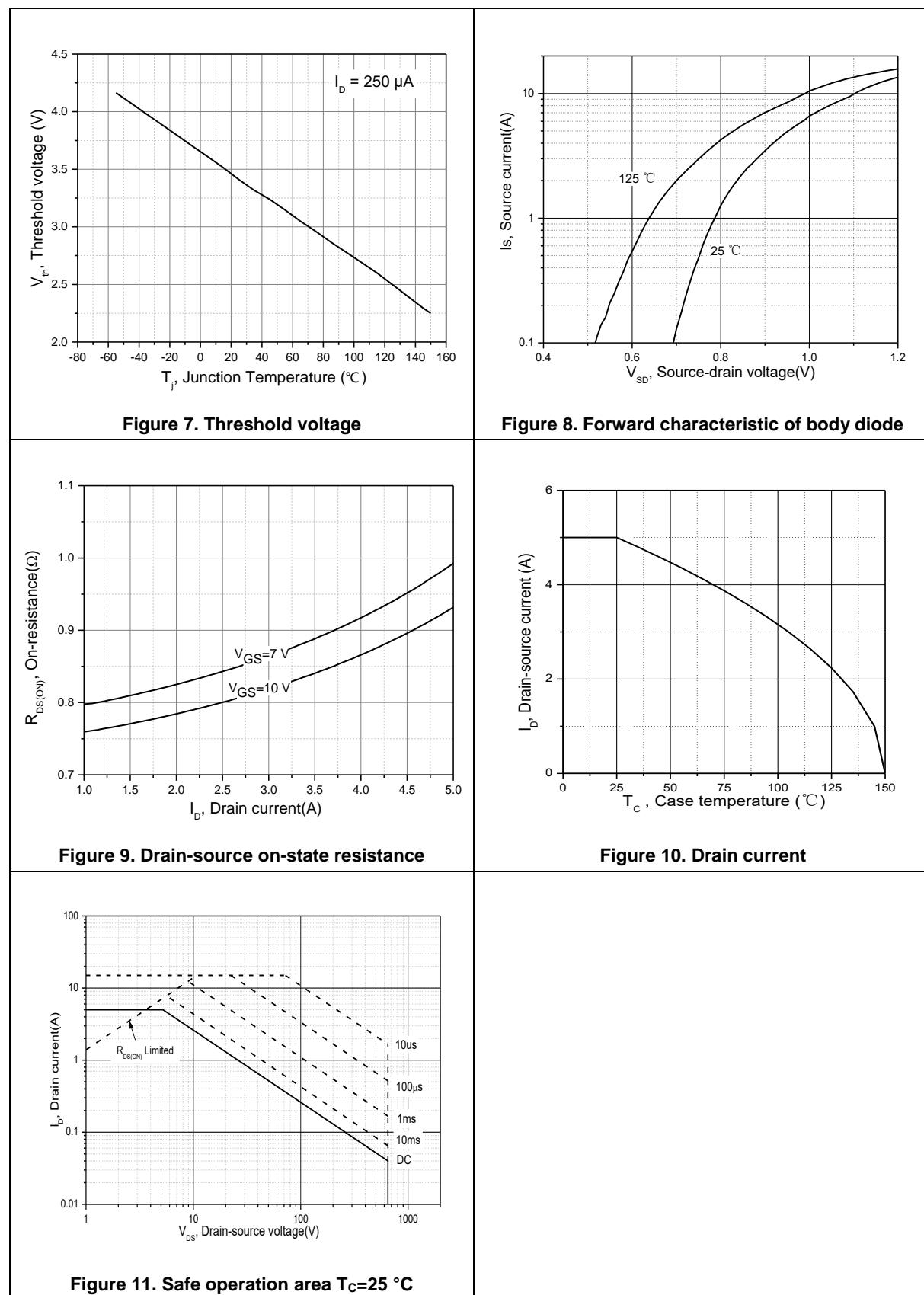
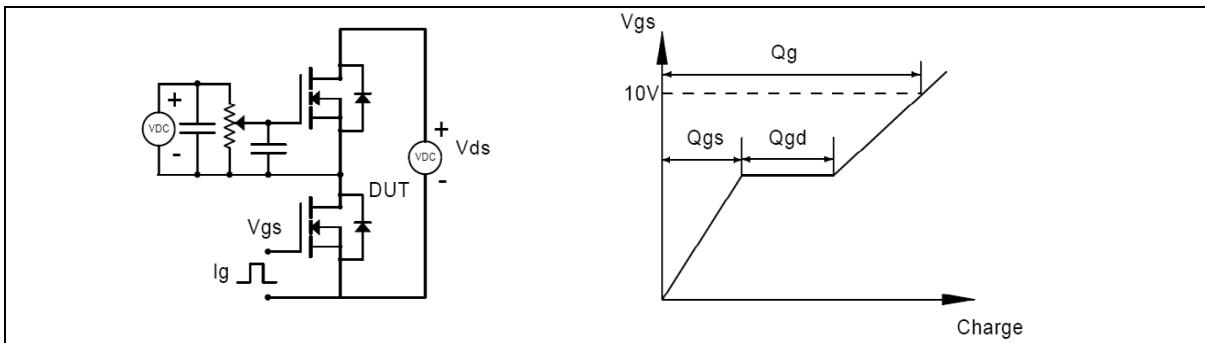


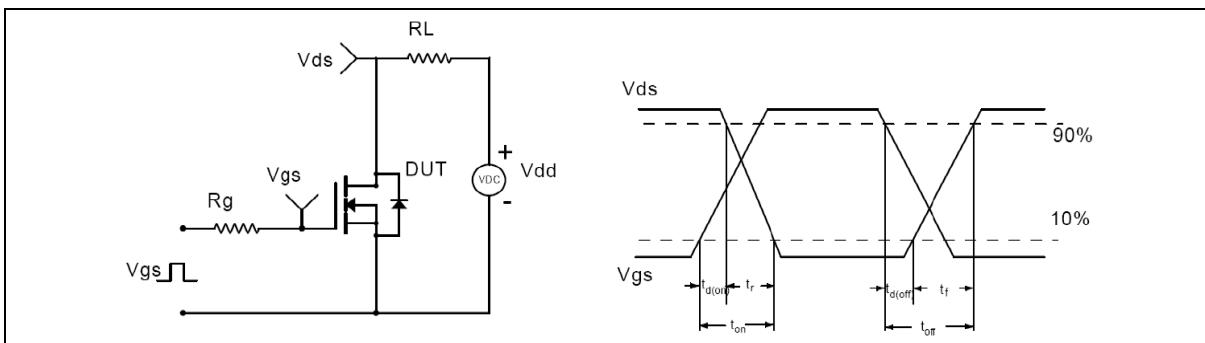
Figure 6. Drain-source on-state resistance



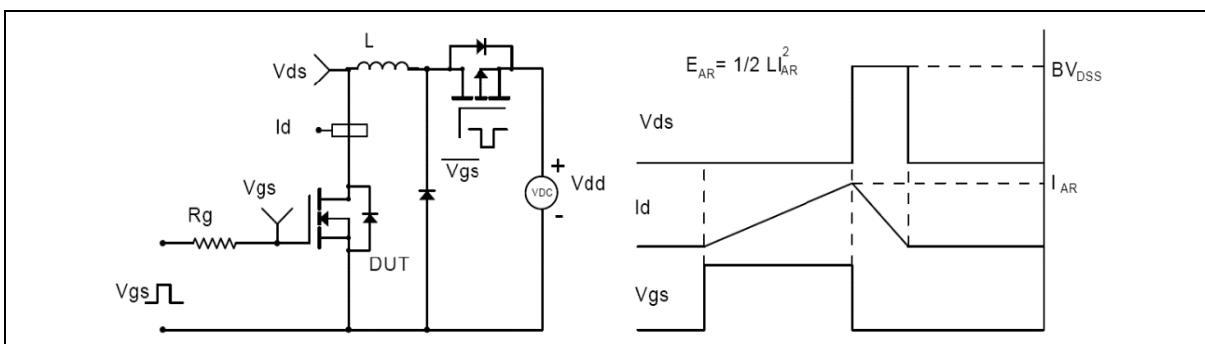
### Test circuits and waveforms



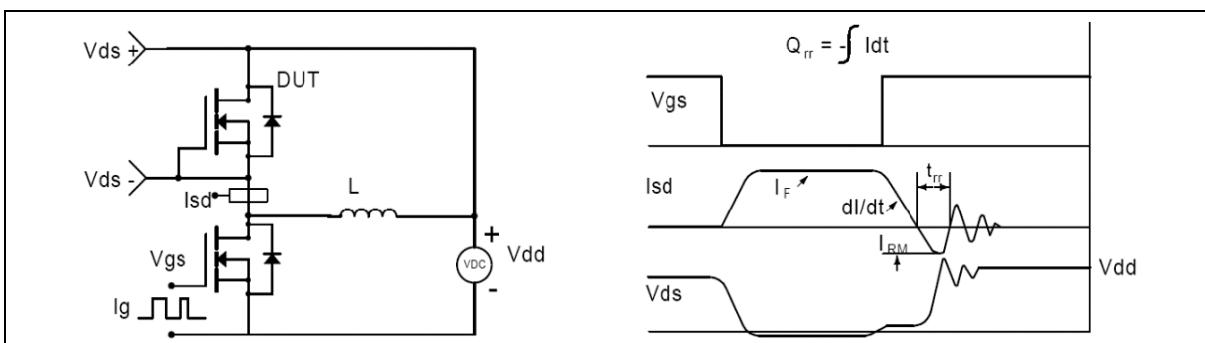
**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

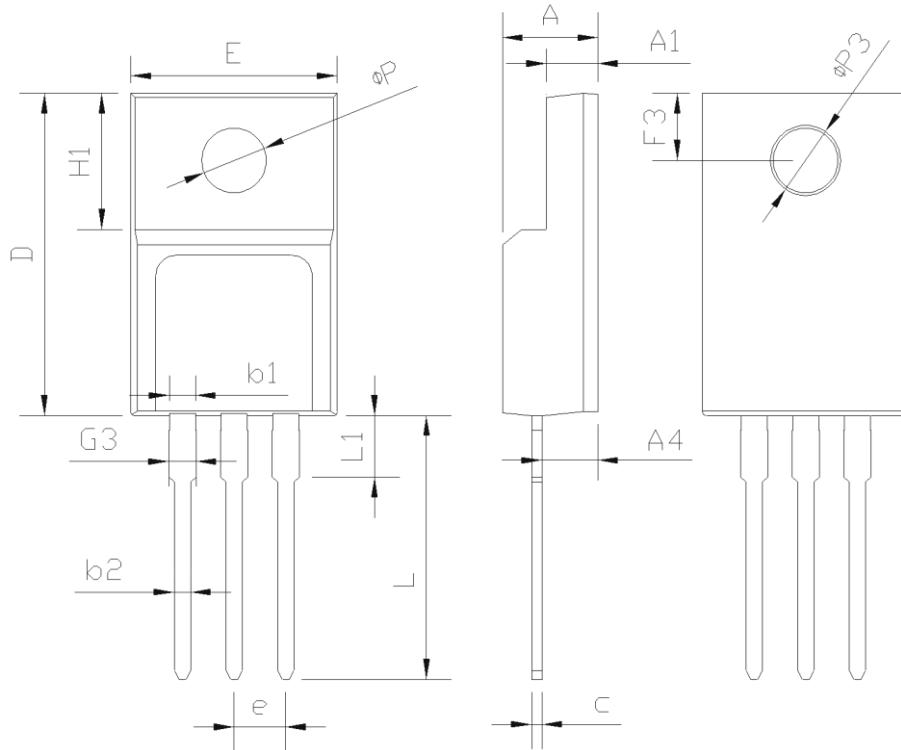


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

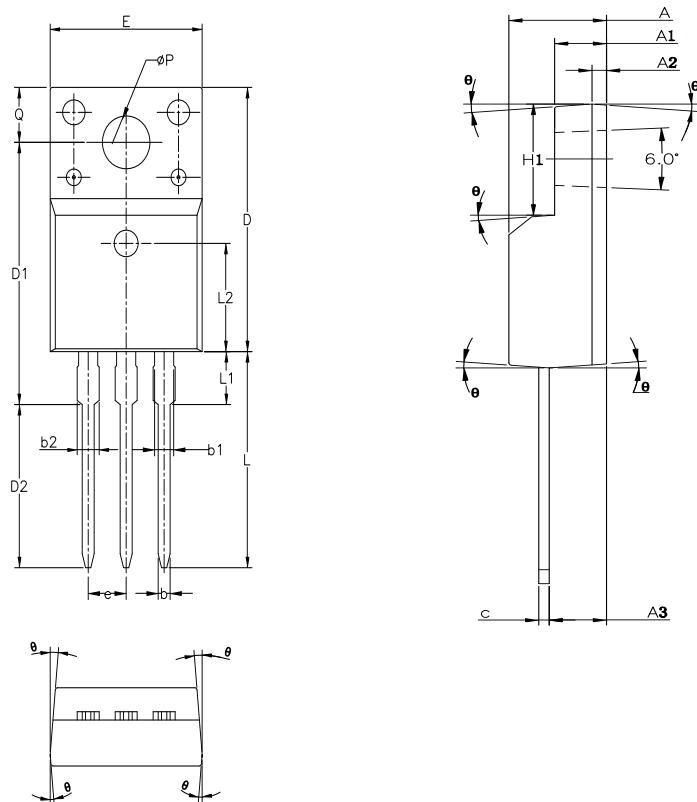
## Package Information



Symbol	mm		
	Min	Nom	Max
E	9.96	10.16	10.36
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.56	2.76	2.96
c	0.40	0.50	0.65
D	15.57	15.87	16.17
H1	6.70REF		
e	2.54BSC		
L	12.68	12.98	13.28
L1	2.88	3.03	3.18
ΦP	3.03	3.18	3.38
ΦP3	3.15	3.45	3.65
F3	3.15	3.30	3.45
G3	1.25	1.35	1.55
b1	1.18	1.28	1.43
b2	0.70	0.80	0.95

Version 1: TO220F-C package outline dimension

## Package Information



Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	-	0.90
b1	1.27	-	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	-	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54BSC		
e1	5.08BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	-	-	3.50
L2	4.60REF		
ΦP	3.55	3.60	3.65
Q	2.73	-	2.87
θ1	1°	3°	5°

Version 2: TO220F-J package outline dimension

## Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO220F-C	50	20	1000	6	6000
TO220F-J	50	20	1000	5	5000

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R900FEF	TO220F	yes	yes	yes

## Legal Disclaimer

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