

General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Z series is integrated with fast recovery diode (FRD) to minimize reverse recovery time. It is suitable for resonant switching topologies to reach higher efficiency, higher reliability and smaller form factor.

Features

- Low $R_{DS(on)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Ultra-fast and robust body diode



Applications

- PC power
- Telecom power
- Server power
- EV Charger
- Motor driver

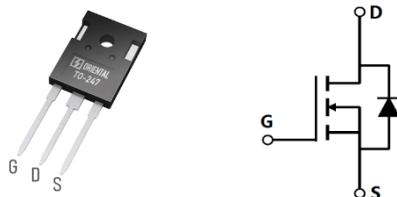
Key Performance Parameters

Parameter	Value	Unit
V_{DS} , min @ $T_j(max)$	700	V
I_D , pulse	75	A
$R_{DS(ON)}$, max @ $V_{GS}=10V$	140	mΩ
Q_g	55.2	nC

Marking Information

Product Name	Package	Marking
OSG65R140HSZF	TO247	OSG65R140HSZ

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	650	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25^\circ\text{C}$	I_D	25	A
Continuous drain current ¹⁾ , $T_C=100^\circ\text{C}$		16	
Pulsed drain current ²⁾ , $T_C=25^\circ\text{C}$	$I_{D, \text{pulse}}$	75	A
Continuous diode forward current ¹⁾ , $T_C=25^\circ\text{C}$	I_S	25	A
Diode pulsed current ²⁾ , $T_C=25^\circ\text{C}$	$I_{S, \text{pulse}}$	75	A
Power dissipation ³⁾ , $T_C=25^\circ\text{C}$	P_D	219	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	1000	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\dots 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\dots 480\text{ V}$, $I_{SD} \leq I_D$	dv/dt	50	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.57	°C/W
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	62	°C/W

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	650			V	$V_{GS}=0\text{ V}, I_D=1\text{ mA}$
		700	750			$V_{GS}=0\text{ V}, I_D=1\text{ mA}, T_j=150^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	3.0		4.5	V	$V_{DS}=V_{GS}, I_D=1\text{ mA}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		0.12	0.14	Ω	$V_{GS}=10\text{ V}, I_D=12.5\text{ A}$
			0.30			$V_{GS}=10\text{ V}, I_D=12.5\text{ A}, T_j=150^\circ\text{C}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	I_{DSS}			10	μA	$V_{DS}=650\text{ V}, V_{GS}=0\text{ V}$
Gate resistance	R_G		16.7		Ω	$f=1\text{ MHz}$, Open drain

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		2855.4		pF	V _{GS} =0 V, V _{DS} =50 V, f=100 kHz
Output capacitance	C _{oss}		151.1		pF	
Reverse transfer capacitance	C _{rss}		7.7		pF	
Turn-on delay time	t _{d(on)}		56.3		ns	
Rise time	t _r		68.8		ns	
Turn-off delay time	t _{d(off)}		108.5		ns	
Fall time	t _f		31.6		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		55.2		nC	V _{GS} =10 V, V _{DS} =400 V, R _G =2 Ω, I _D =16 A
Gate-source charge	Q _{gs}		13.7		nC	
Gate-drain charge	Q _{gd}		24.0		nC	
Gate plateau voltage	V _{plateau}		6.8		V	

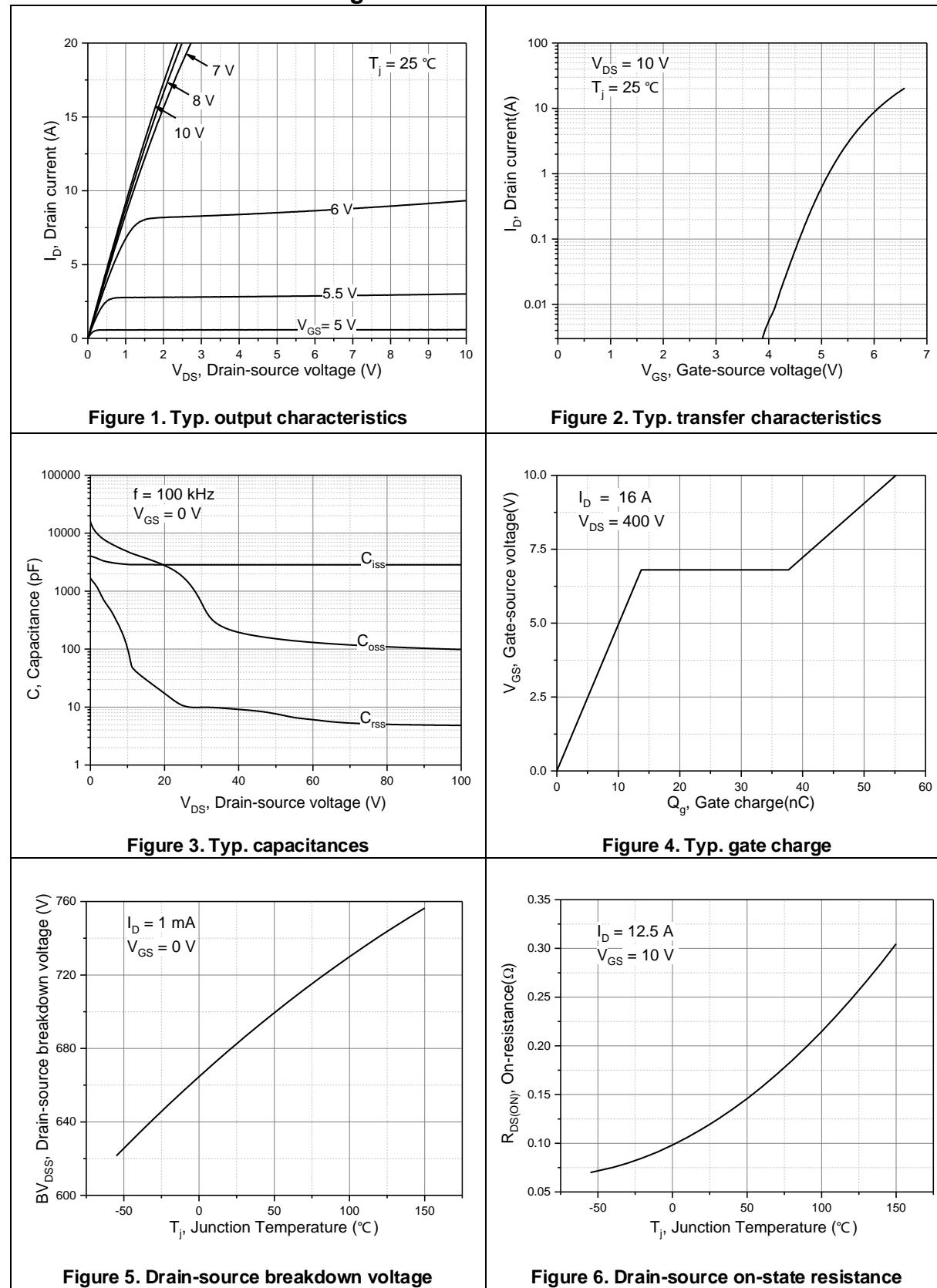
Body Diode Characteristics

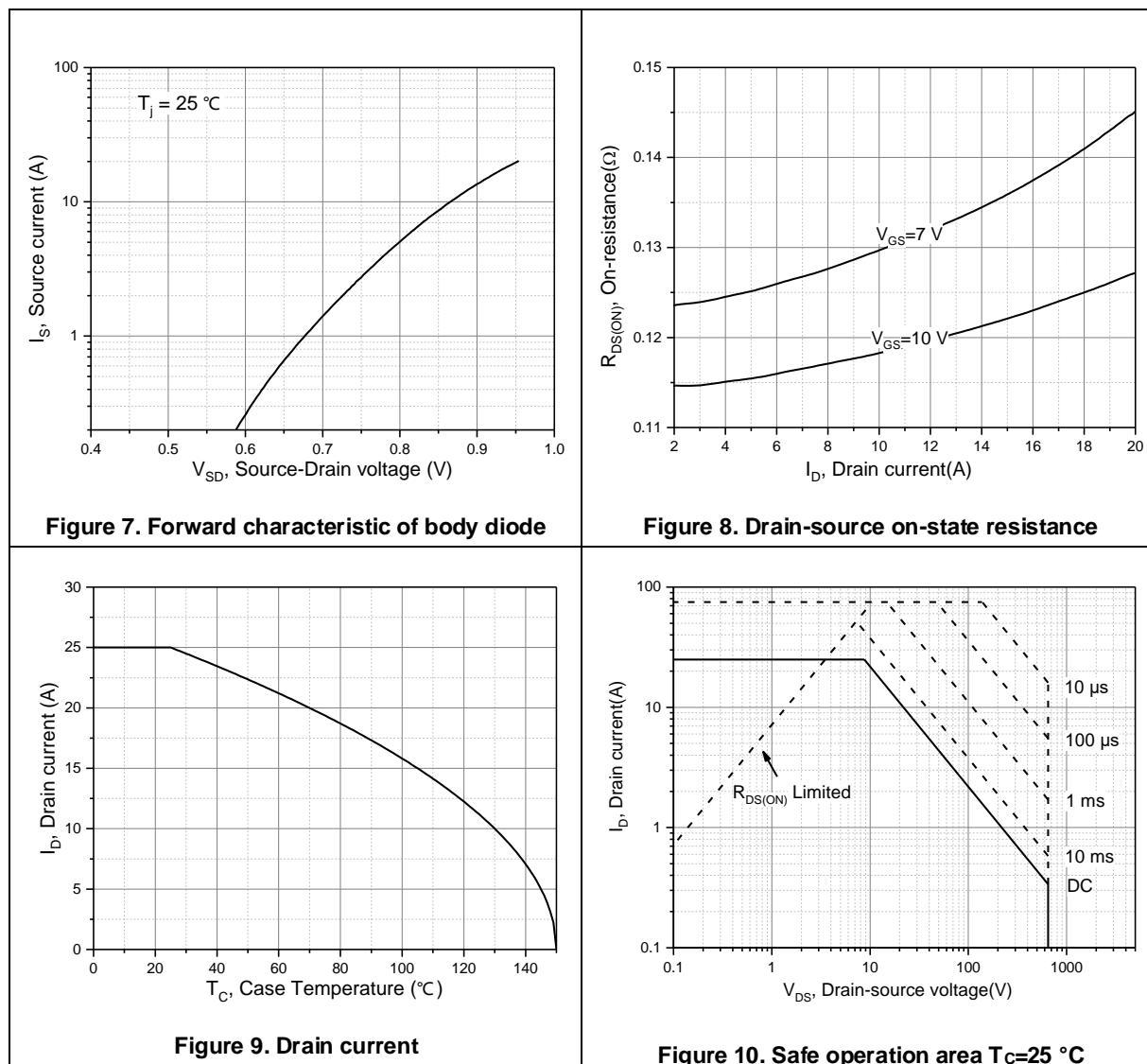
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V _{SD}			1.3	V	I _S =25 A, V _{GS} =0 V
Reverse recovery time	t _{rr}		126.0		ns	
Reverse recovery charge	Q _{rr}		0.7		μC	
Peak reverse recovery current	I _{rrm}		10.6		A	

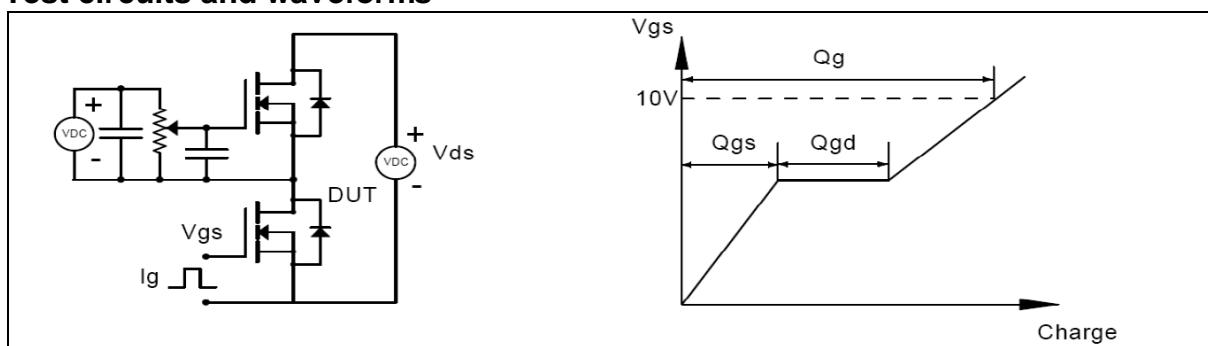
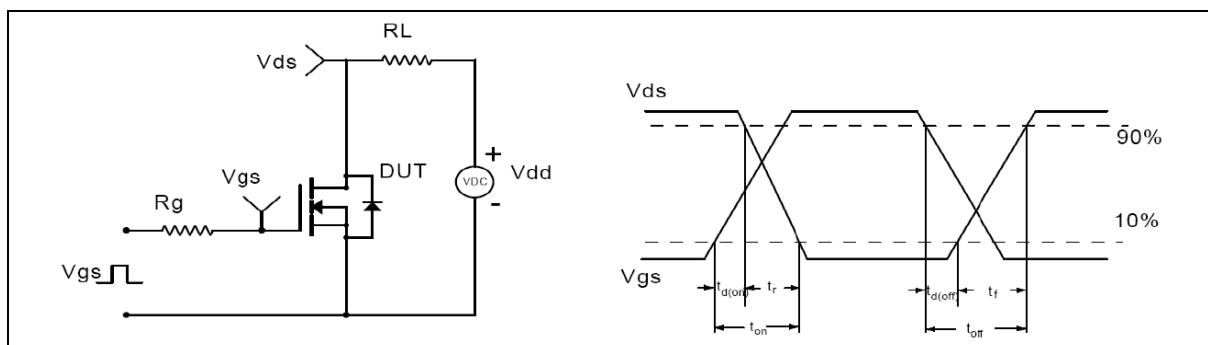
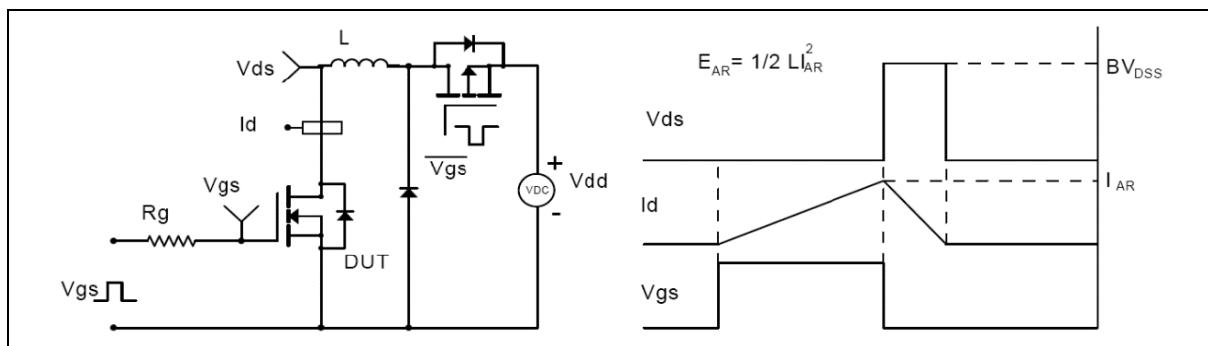
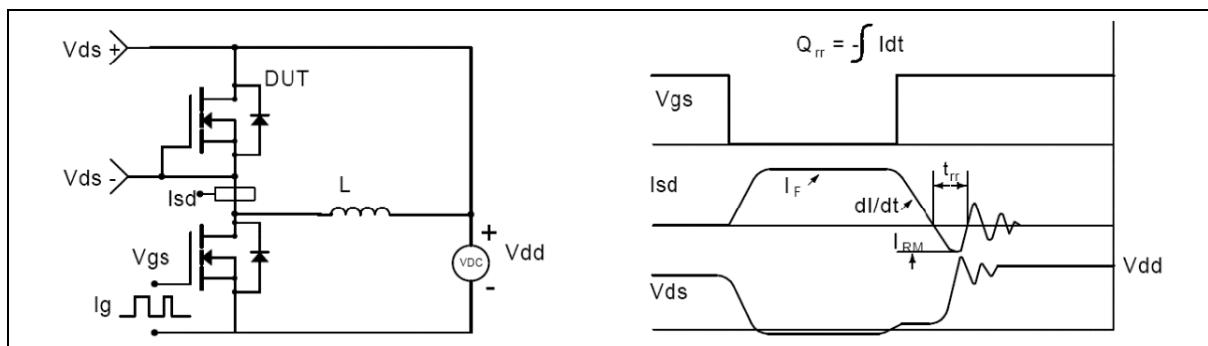
Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R_{θJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
- 5) V_{DD}=100 V, V_{GS}=10 V, L=60 mH, starting T_j=25 °C.

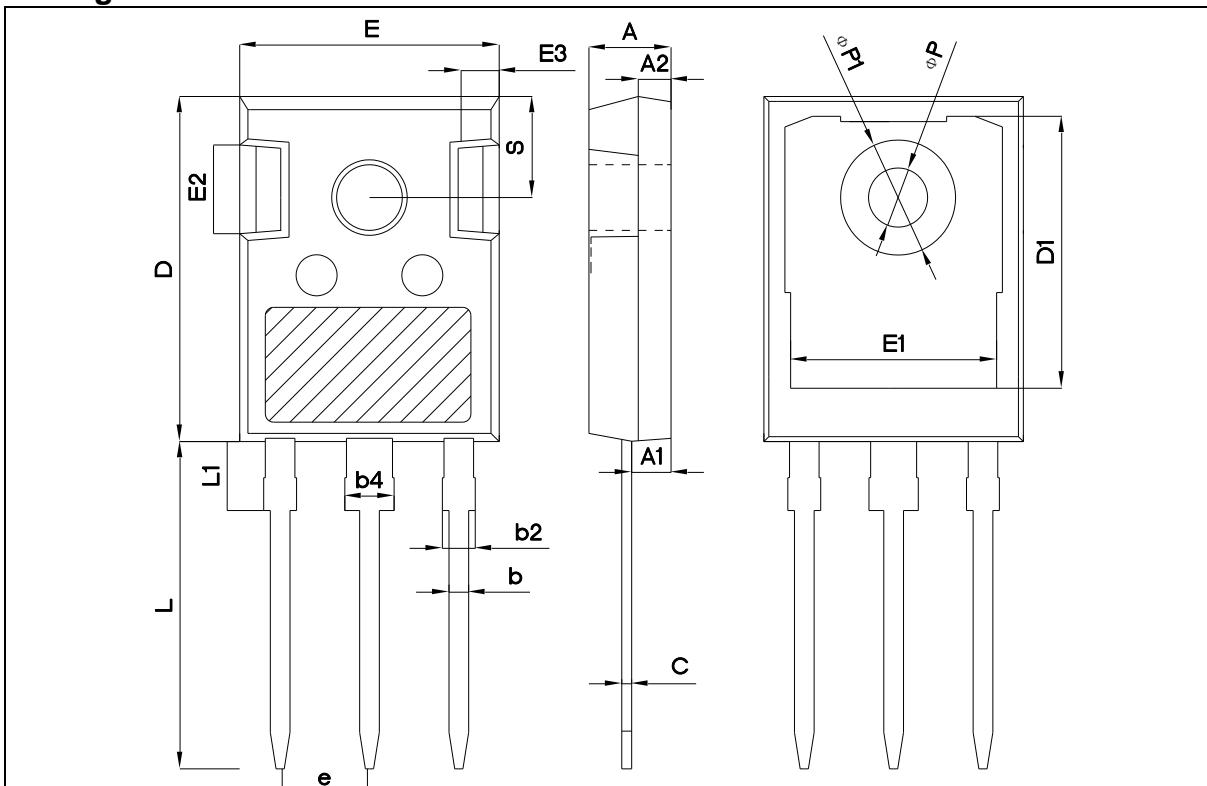
Electrical Characteristics Diagrams





Test circuits and waveforms

Figure 1. Gate charge test circuit & waveform

Figure 2. Switching time test circuit & waveforms

Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

Figure 4. Diode reverse recovery test circuit & waveforms

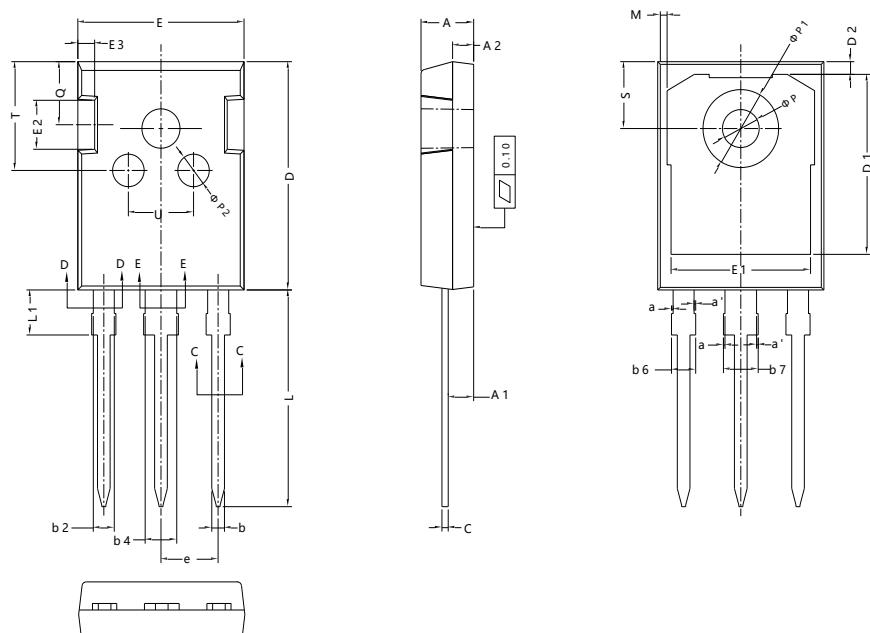
Package Information



Symbol	mm		
	Min	Nom	Max
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.80	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44 BSC		
L	19.82	19.92	20.22
L1	-	-	4.30
ΦP	3.40	3.60	3.80
ΦP1	-	-	7.30
S	6.15 BSC		

Version1: TO247-C package outline dimension

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.90	5.00	5.10
A1	2.31	2.41	2.51
A2	1.90	2.00	2.10
a	0.00	-	0.15
a'	0.00	-	0.15
b	1.16	-	1.26
b2	1.96	-	2.06
b4	2.96	-	3.06
b6	-	-	2.25
b7	-	-	3.25
c	0.59	-	0.66
c1	0.58	0.60	0.62
D	20.90	21.00	21.10
D1	16.25	16.55	16.85
D2	1.05	1.17	1.35
E	15.70	15.80	15.90
E1	13.10	13.30	13.50
E2	4.40	4.50	4.60
E3	1.50	1.60	1.70
e	5.436 BSC		
L	19.80	19.92	20.10
L1	-	-	4.30
M	0.35	-	0.95
P	3.40	3.50	3.60
P1	7.00	-	7.40
P2	2.40	2.5	2.6
Q	5.60	-	6.0
S	6.05	6.15	6.25
T	9.8	-	10.20
U	6.00	-	6.40

Version2: TO247-J package outline dimension

Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO247-C	30	11	330	6	1980
TO247-J	30	20	600	5	3000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG65R140HSZF	TO247	yes	yes	yes

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