

## General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent stability and uniformity



## Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

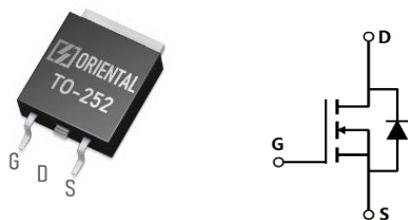
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS, min} @ T_{j(max)}$	650	V
$I_D, pulse$	6	A
$R_{DS(ON)}, max @ V_{GS}=10V$	2.2	$\Omega$
$Q_g$	5.1	nC

## Marking Information

Product Name	Package	Marking
OSG60R2K2DF	TO252	OSG60R2K2D

## Package & Pin Information



**Absolute Maximum Ratings** at  $T_j=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	$V_{DS}$	600	V
Gate-source voltage	$V_{GS}$	$\pm 30$	V
Continuous drain current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_D$	2	A
Continuous drain current <sup>1)</sup> , $T_C=100^\circ\text{C}$		1.26	
Pulsed drain current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{D,\text{pulse}}$	6	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_S$	2	A
Diode pulsed current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{S,\text{pulse}}$	6	A
Power dissipation <sup>3)</sup> , $T_C=25^\circ\text{C}$	$P_D$	20	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	32	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\ldots 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\ldots 480\text{ V}$ , $I_{SD} \leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	°C

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	6.25	°C/W
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62	°C/W

**Electrical Characteristics** at  $T_j=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	600			V	$V_{GS}=0\text{ V}$ , $I_D=250\text{ uA}$
		650				$V_{GS}=0\text{ V}$ , $I_D=250\text{ uA}$ , $T_j=150^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.9		3.9	V	$V_{DS}=V_{GS}$ , $I_D=250\text{ uA}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		1.9	2.2	$\Omega$	$V_{GS}=10\text{ V}$ , $I_D=1\text{ A}$
			5.0			$V_{GS}=10\text{ V}$ , $I_D=1\text{ A}$ , $T_j=150^\circ\text{C}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=600\text{ V}$ , $V_{GS}=0\text{ V}$
Gate resistance	$R_G$		467		$\Omega$	$f=1\text{ MHz}$ , Open drain

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C <sub>iss</sub>		147		pF	V <sub>GS</sub> =0 V, V <sub>DS</sub> =50 V, f=100 KHz
Output capacitance	C <sub>oss</sub>		11		pF	
Reverse transfer capacitance	C <sub>rss</sub>		0.9		pF	
Effective output capacitance, energy related	C <sub>o(er)</sub>		7		pF	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V-400V
Effective output capacitance, time related	C <sub>o(tr)</sub>		29		pF	
Turn-on delay time	t <sub>d(on)</sub>		56.5		ns	V <sub>GS</sub> =10 V, V <sub>DS</sub> =400 V, R <sub>G</sub> =2 Ω, I <sub>D</sub> =1 A
Rise time	t <sub>r</sub>		35		ns	
Turn-off delay time	t <sub>d(off)</sub>		128.5		ns	
Fall time	t <sub>f</sub>		62.5		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q <sub>g</sub>		5.1		nC	V <sub>GS</sub> =10 V, V <sub>DS</sub> =400 V, I <sub>D</sub> =1 A
Gate-source charge	Q <sub>gs</sub>		1.1		nC	
Gate-drain charge	Q <sub>gd</sub>		1.5		nC	
Gate plateau voltage	V <sub>plateau</sub>		5.2		V	

### Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V <sub>SD</sub>			1.3	V	I <sub>S</sub> =2 A, V <sub>GS</sub> =0 V
Reverse recovery time	t <sub>rr</sub>		107		ns	
Reverse recovery charge	Q <sub>rr</sub>		0.4		μC	
Peak reverse recovery current	I <sub>rrm</sub>		7.4		A	

### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R<sub>θJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.
- 5) V<sub>DD</sub>=100 V, V<sub>GS</sub>=10 V, L=79.9 mH, starting T<sub>j</sub>=25 °C.

## Electrical Characteristics Diagrams

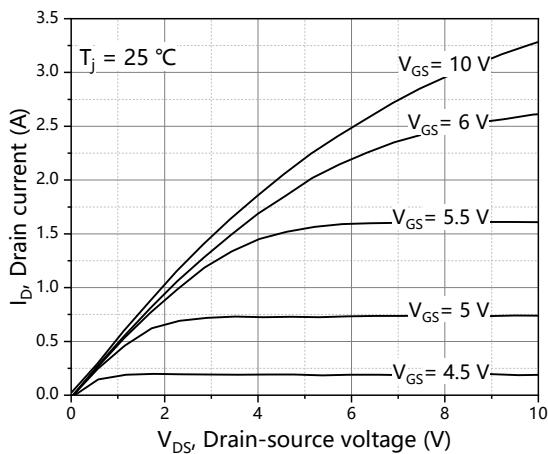


Figure 1. Typ. output characteristics

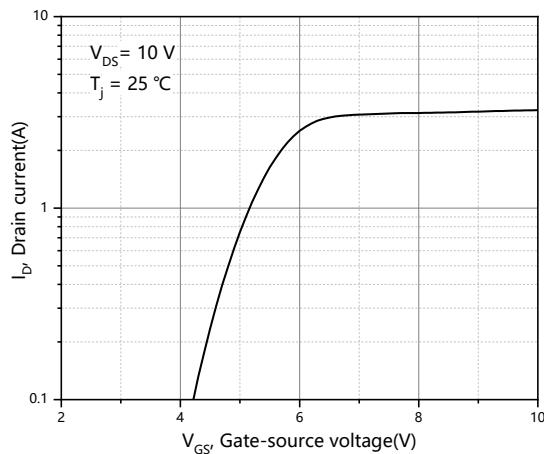


Figure 2. Typ. transfer characteristics

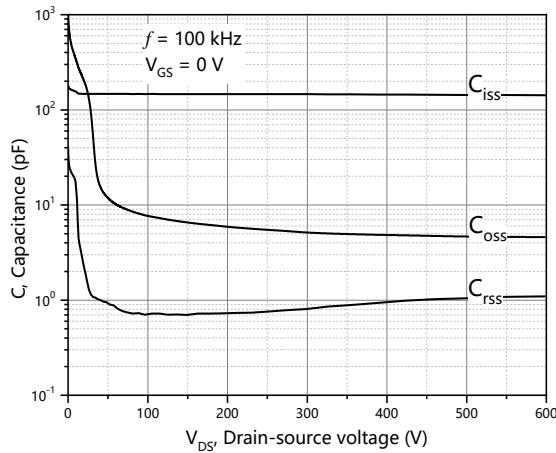


Figure 3. Typ. capacitances

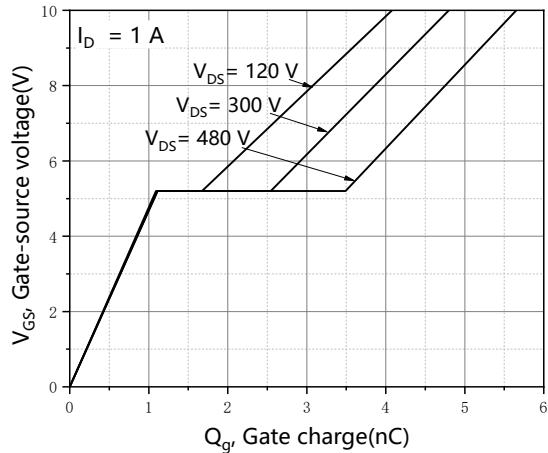


Figure 4. Typ. gate charge

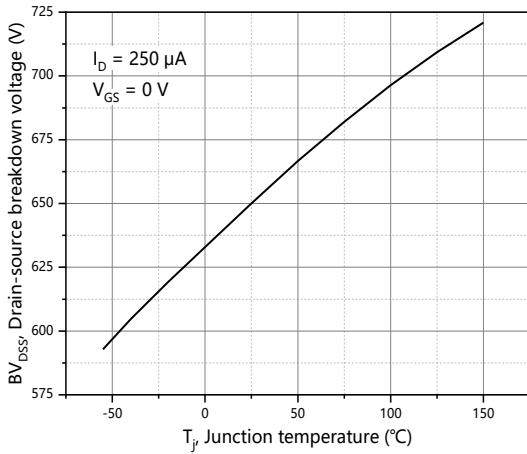


Figure 5. Drain-source breakdown voltage

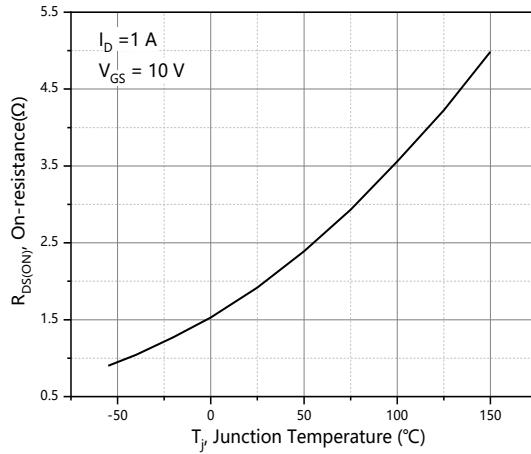
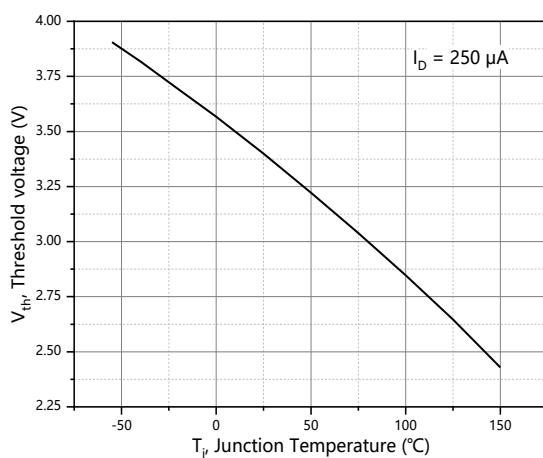
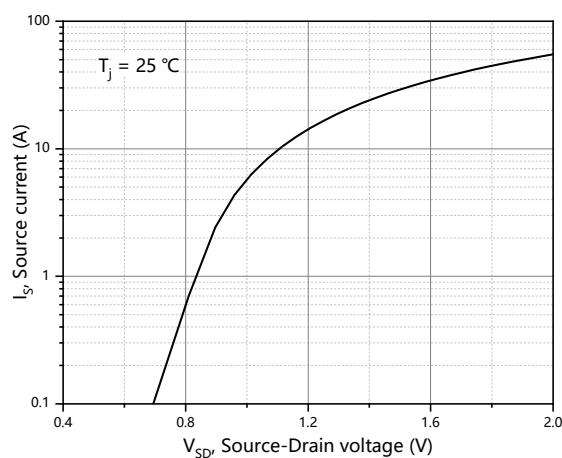
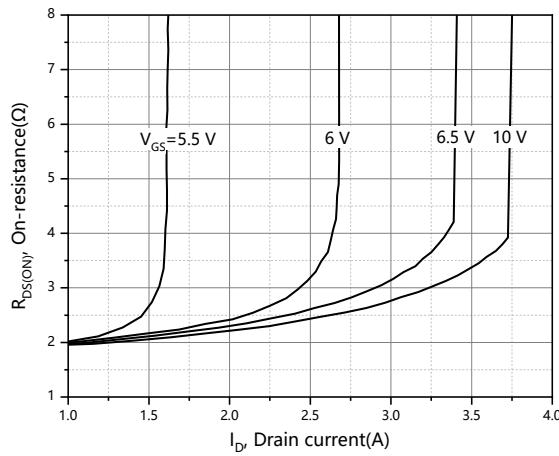
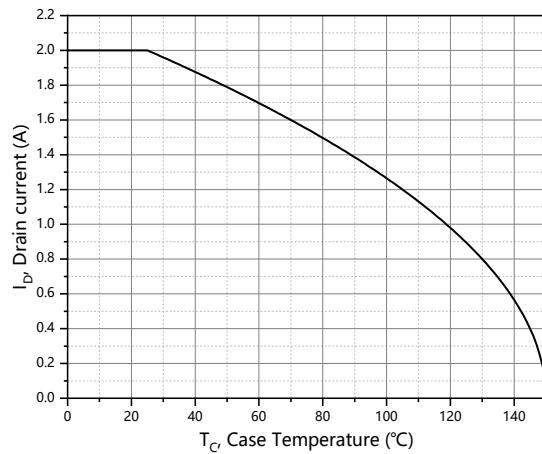
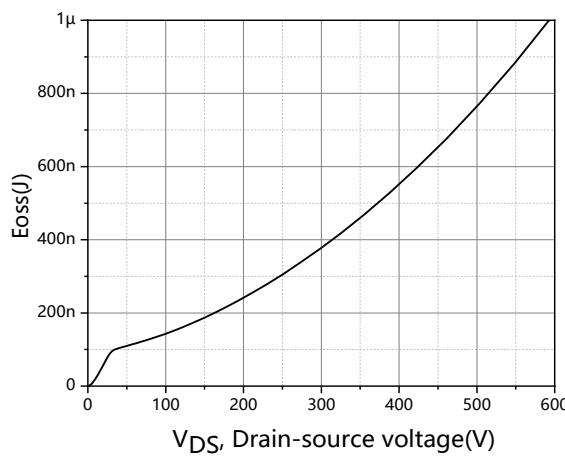
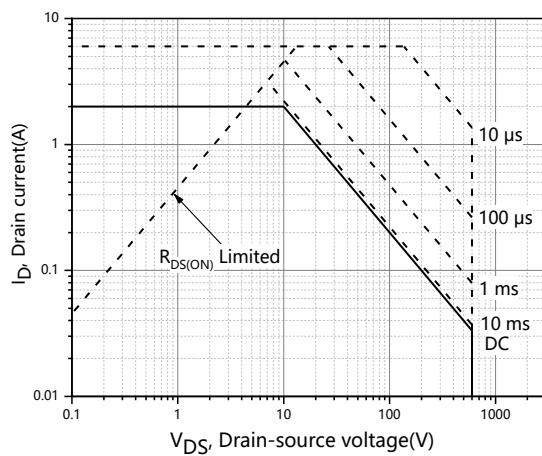
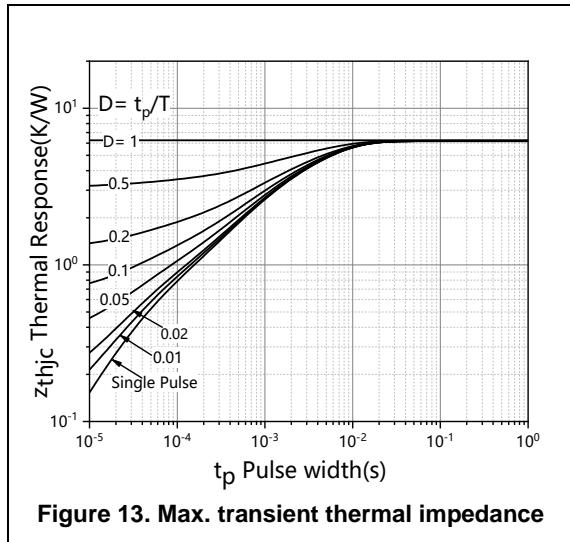
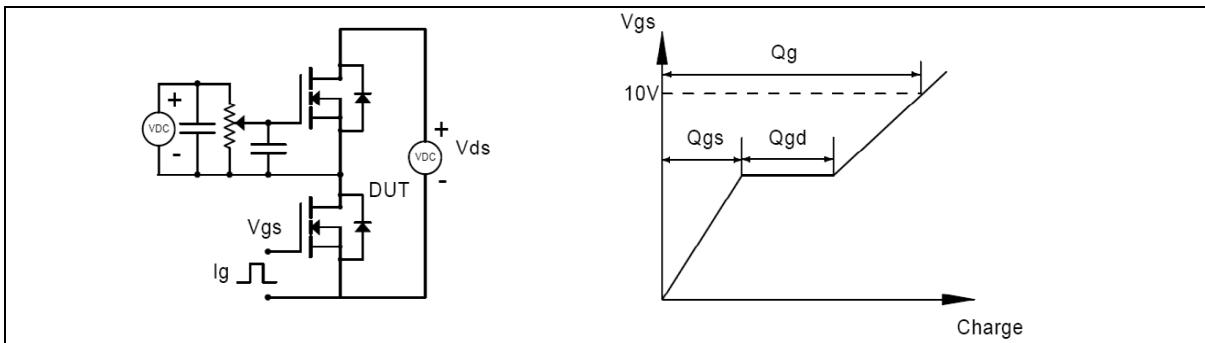


Figure 6. Drain-source on-state resistance

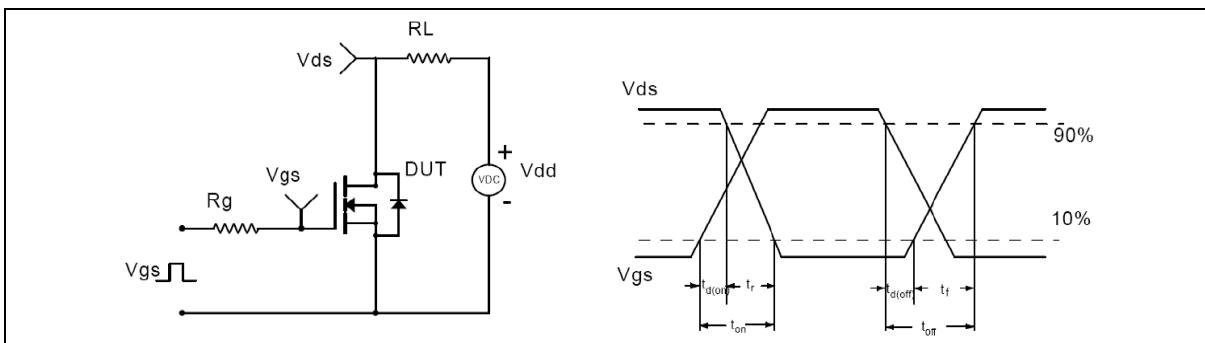

**Figure 7. Threshold voltage**

**Figure 8. Forward characteristic of body diode**

**Figure 9. Drain-source on-state resistance**

**Figure 10. Drain current**

**Figure 11. Typ. Coss stored energy**

**Figure 12. Safe operation area  $T_C=25^\circ C$**



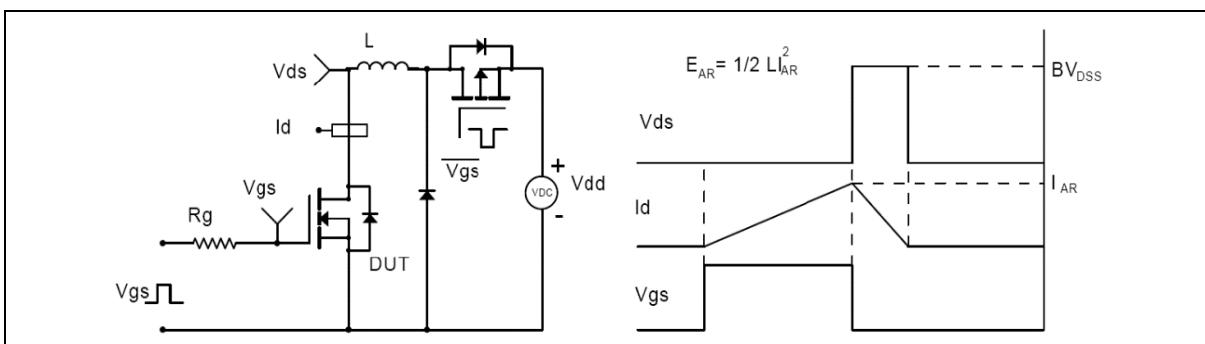
### Test circuits and waveforms



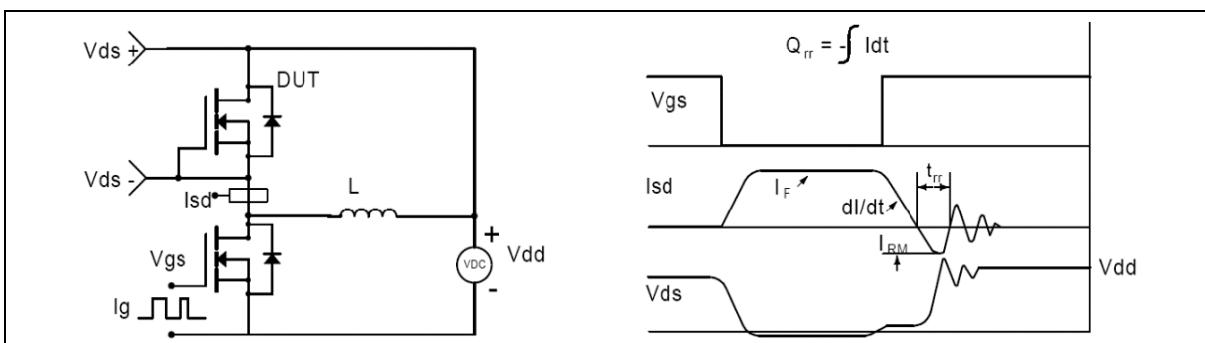
**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

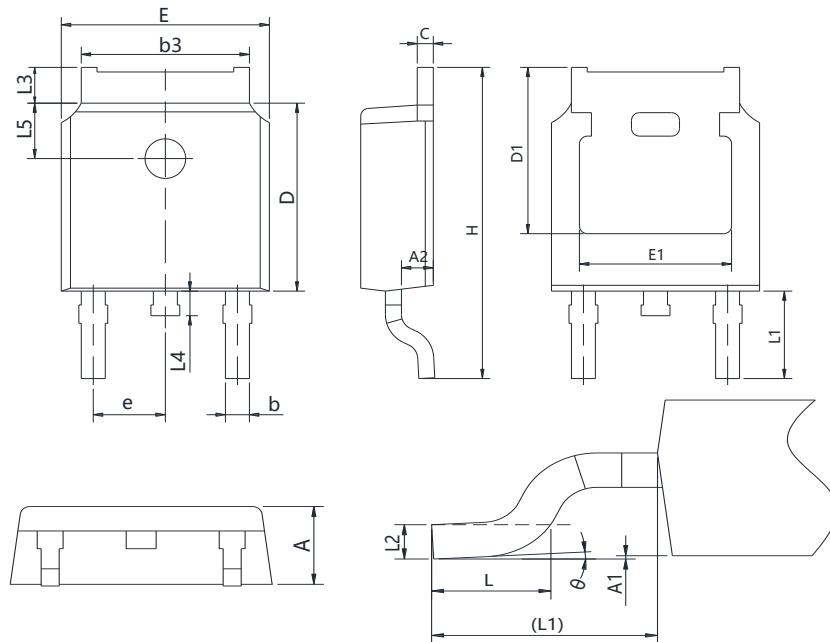


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

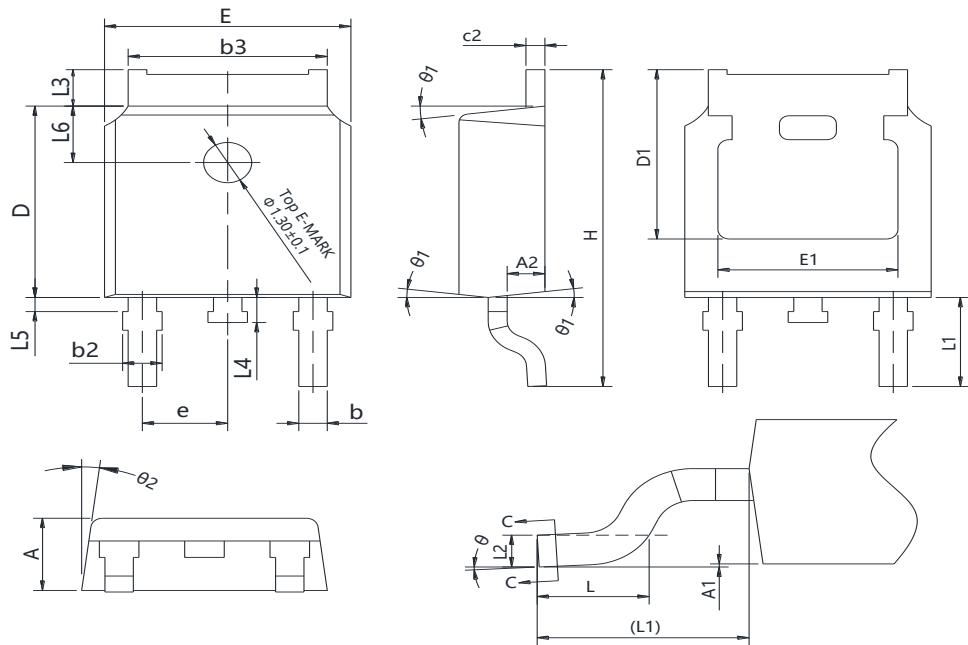
## Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.20
A2	0.97	1.07	1.17
b	0.68	0.78	0.90
b3	5.20	5.33	5.46
c	0.43	0.53	0.61
D	5.98	6.10	6.22
D1	5.30 REF		
E	6.40	6.60	6.73
E1	4.63	-	-
e	2.286 BSC		
H	9.40	10.10	10.50
L	1.38	1.50	1.75
L1	2.90 REF		
L2	0.51 BSC		
L3	0.88	-	1.28
L4	0.50	-	1.00
θ	0°	-	8°

Version 1: TO252-C package outline dimension

## Package Information



Symbol	mm		
	Min	Nom	Max
A	2.20	2.30	2.38
A1	0.00	-	0.10
A2	0.90	1.01	1.10
b	0.72	-	0.85
b1	0.71	0.76	0.81
b2	0.72	-	0.90
b3	5.13	5.33	5.46
c	0.47	-	0.60
c1	0.46	0.51	0.56
c2	0.47	-	0.60
D	6.00	6.10	6.20
D1	5.25	-	-
E	6.50	6.60	6.70
E1	4.70	-	-
e	2.186	2.286	2.386
H	9.80	10.10	10.40
L	1.40	1.50	1.70
L1	2.90 REF		
L2	0.508 BSC		
L3	0.90	-	1.25
L4	0.60	0.80	1.00
L5	0.15	-	0.75
L6	1.80 REF		
θ	0°	-	8°
θ1	5°	7°	9°
θ2	5°	7°	9°

Version 2: TO252-J package outline dimension

## Ordering Information

Package Type	Units/Reel	Reels / Inner Box	Units/Inner Box	Inner Box/Carton Box	Units/Carton Box
TO252-C	2500	2	5000	5	25000
TO252-J	2500	2	5000	5	25000

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG60R2K2DF	TO252	yes	yes	yes

## Legal Disclaimer

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