

General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity



Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

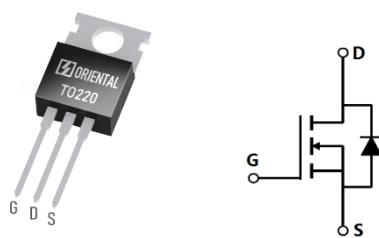
Key Performance Parameters

Parameter	Value	Unit
$V_{DS, min} @ T_{j(max)}$	600	V
$I_D, pulse$	33	A
$R_{DS(ON)}, max @ V_{GS}=10V$	380	mΩ
Q_g	12.5	nC

Marking Information

Product Name	Package	Marking
OSG55R380PF	TO220	OSG55R380P

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	550	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_C=25\text{ }^\circ\text{C}$	I_D	11	A
Continuous drain current ¹⁾ , $T_C=100\text{ }^\circ\text{C}$		7	
Pulsed drain current ²⁾ , $T_C=25\text{ }^\circ\text{C}$	$I_{D,\text{pulse}}$	33	A
Continuous diode forward current ¹⁾ , $T_C=25\text{ }^\circ\text{C}$	I_S	11	A
Diode pulsed current ²⁾ , $T_C=25\text{ }^\circ\text{C}$	$I_{S,\text{pulse}}$	33	A
Power dissipation ³⁾ , $T_C=25\text{ }^\circ\text{C}$	P_D	63	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	200	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\text{...}480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\text{...}480\text{ V}$, $I_{SD}\leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	1.98	$^\circ\text{C/W}$
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	62	$^\circ\text{C/W}$

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	550			V	$V_{GS}=0\text{ V}$, $I_D=250\text{ }\mu\text{A}$
		600				$V_{GS}=0\text{ V}$, $I_D=250\text{ }\mu\text{A}$, $T_j=150\text{ }^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.0		4.0	V	$V_{DS}=V_{GS}$, $I_D=250\text{ }\mu\text{A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		0.30	0.38	Ω	$V_{GS}=10\text{ V}$, $I_D=5.5\text{ A}$
			0.79			$V_{GS}=10\text{ V}$, $I_D=5.5\text{ A}$, $T_j=150\text{ }^\circ\text{C}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=550\text{ V}$, $V_{GS}=0\text{ V}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		549.3		pF	V _{GS} =0 V, V _{DS} =50 V, f=1 MHz
Output capacitance	C _{oss}		63.1		pF	
Reverse transfer capacitance	C _{rss}		2.1		pF	
Turn-on delay time	t _{d(on)}		28		ns	V _{GS} =10 V, V _{DS} =400 V, R _G =2 Ω, I _D =6 A
Rise time	t _r		22.8		ns	
Turn-off delay time	t _{d(off)}		60.3		ns	
Fall time	t _f		18		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		12.5		nC	V _{GS} =10 V, V _{DS} =400 V, I _D =6 A
Gate-source charge	Q _{gs}		2.6		nC	
Gate-drain charge	Q _{gd}		5.7		nC	
Gate plateau voltage	V _{plateau}		5.8		V	

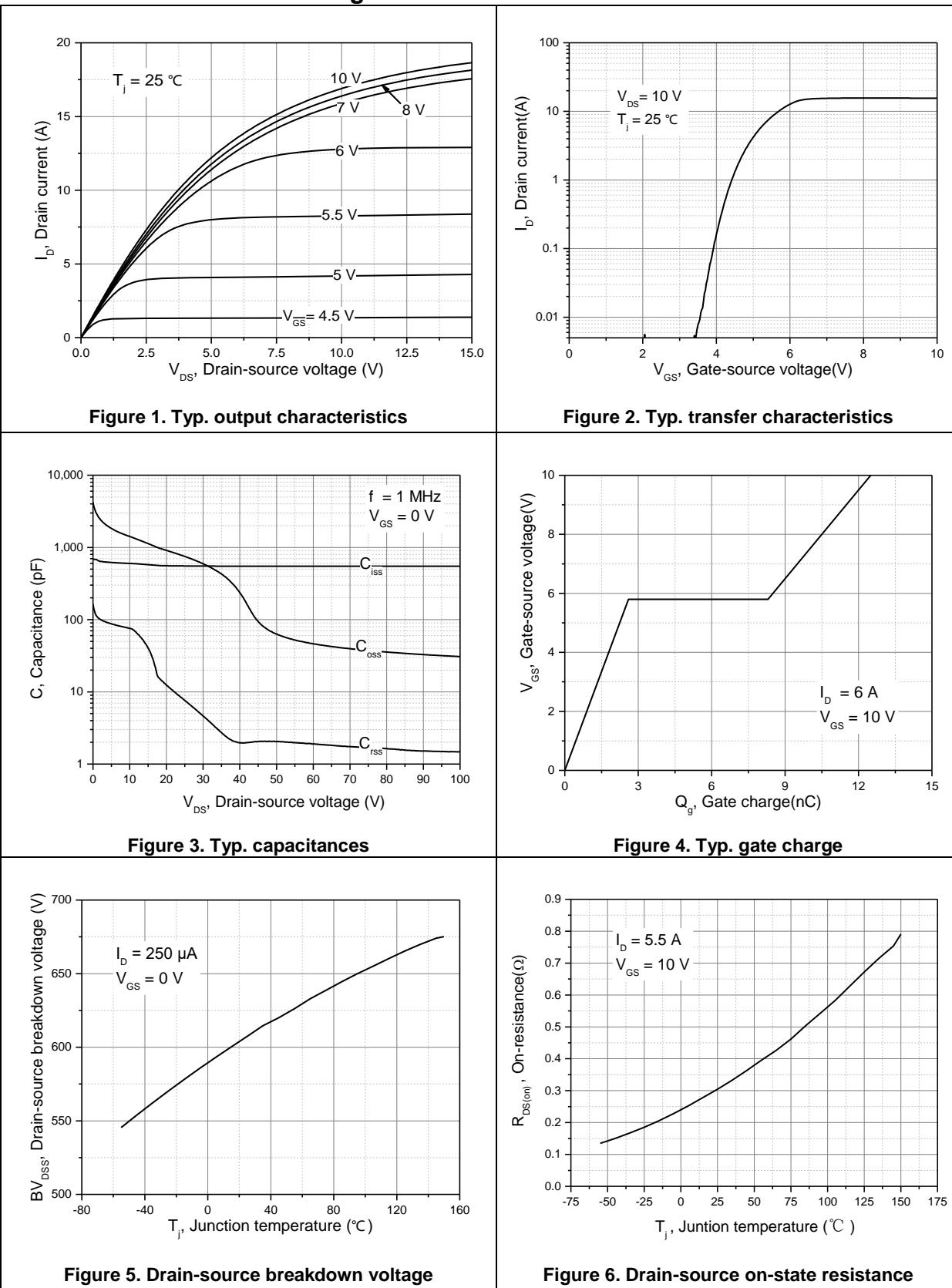
Body Diode Characteristics

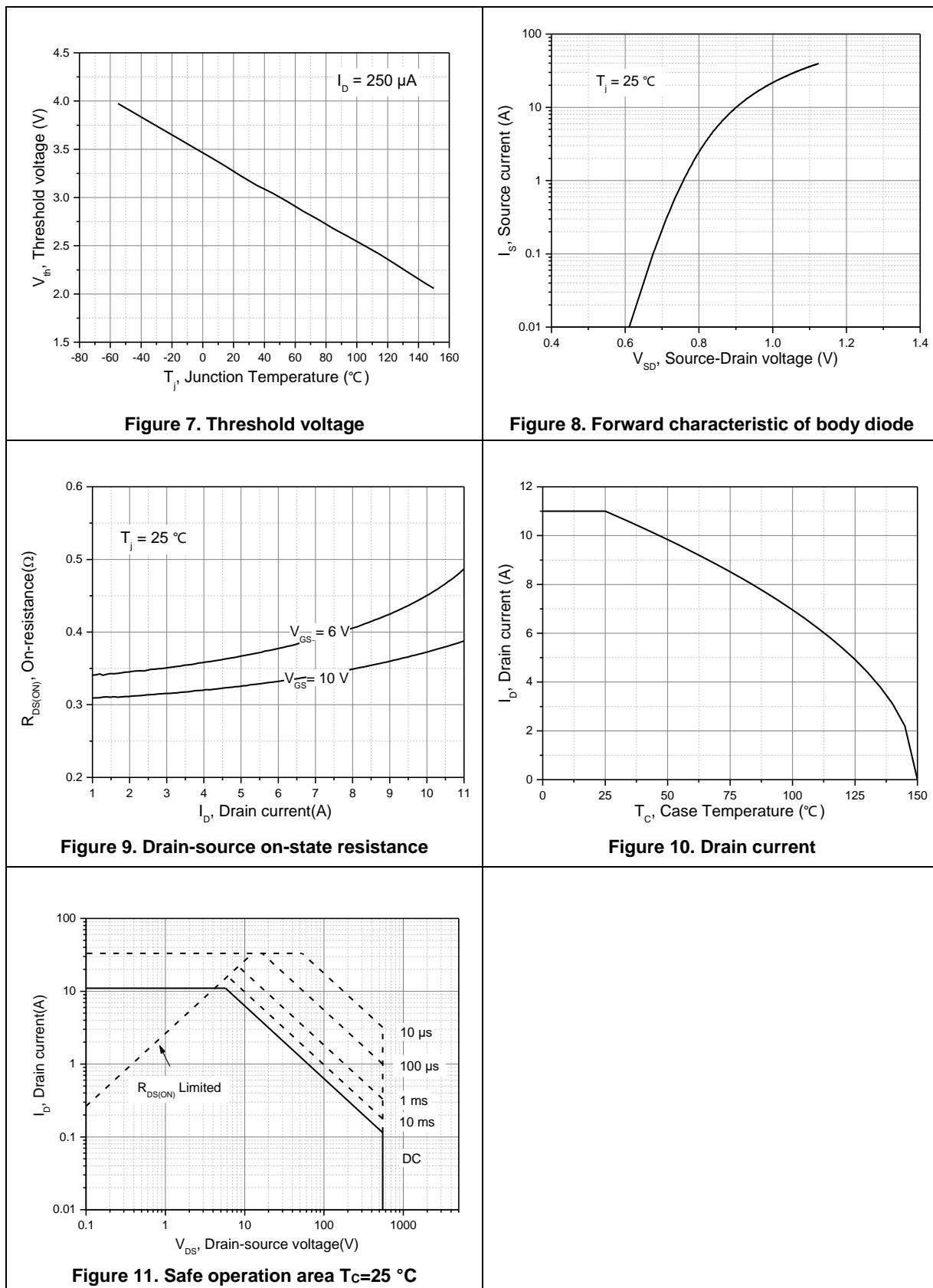
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V _{SD}			1.3	V	I _S =11 A, V _{GS} =0 V
Reverse recovery time	t _{rr}		199		ns	V _R =400 V, I _S =6 A, di/dt=100 A/μs
Reverse recovery charge	Q _{rr}		1.7		μC	
Peak reverse recovery current	I _{rrm}		17.4		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R_{θJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
- 5) V_{DD}=100 V, V_{GS}=10 V, L=10 mH, starting T_j=25 °C.

Electrical Characteristics Diagrams





Test circuits and waveforms



Figure 1. Gate charge test circuit & waveform

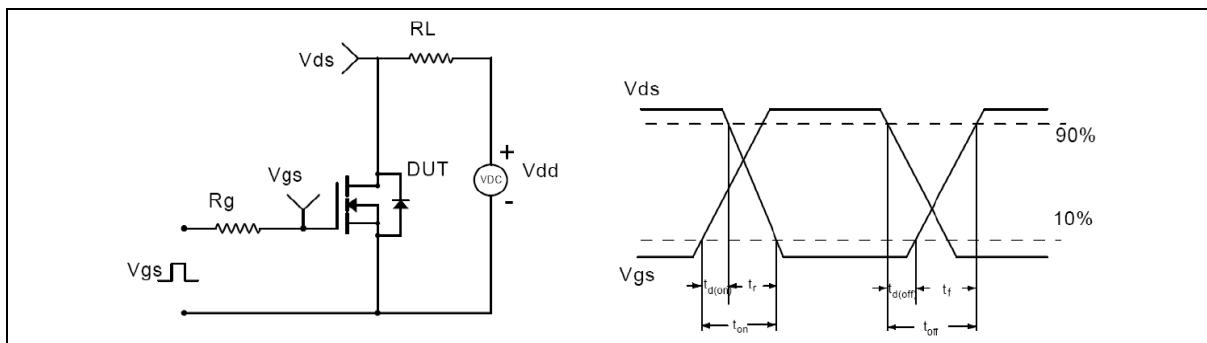


Figure 2. Switching time test circuit & waveforms

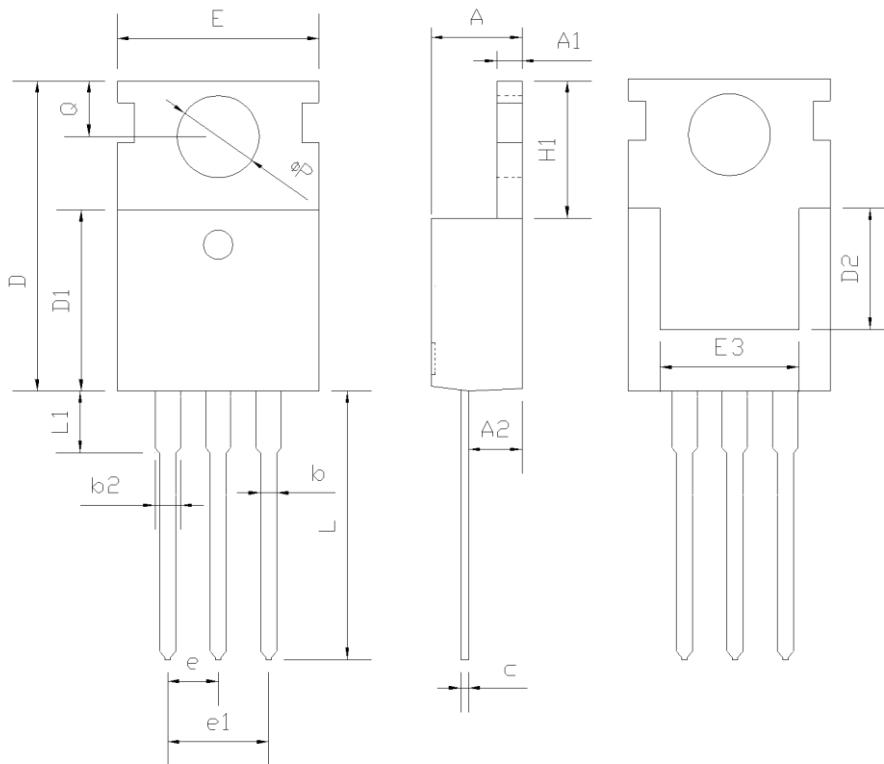


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms



Figure 4. Diode reverse recovery test circuit & waveforms

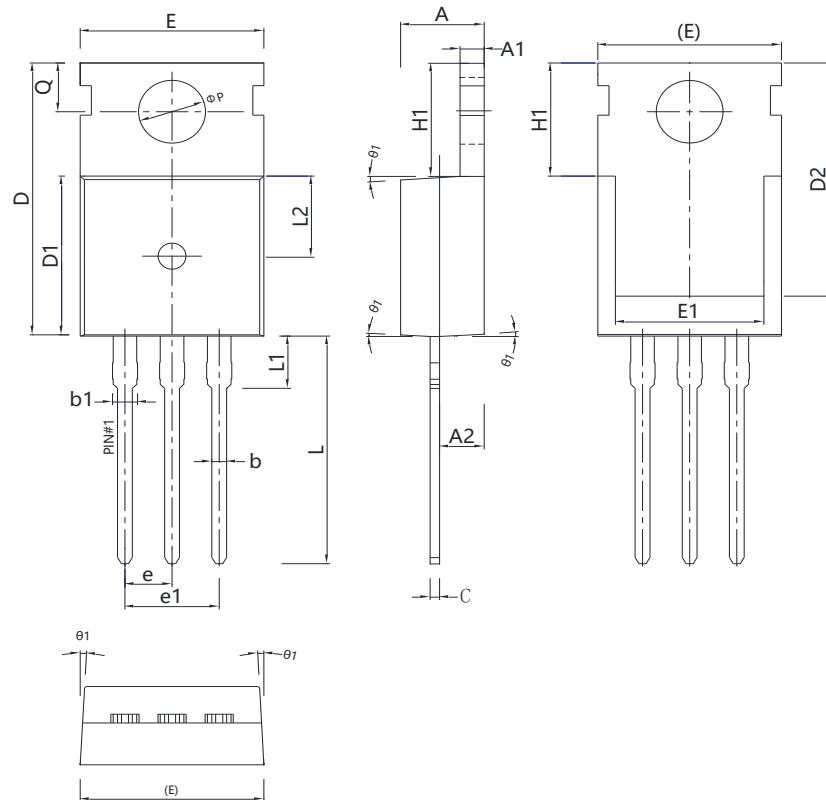
Package Information



Symbol	mm		
	Min	Nom	Max
A	4.37	4.57	4.77
A1	1.25	1.30	1.45
A2	2.20	2.40	2.60
b	0.70	0.80	0.95
b2	1.17	1.27	1.47
c	0.40	0.50	0.65
D	15.10	15.60	16.10
D1	8.80	9.10	9.40
D2	5.50	-	-
E	9.70	10.00	10.30
E3	7.00	-	-
e	2.54 BSC		
e1	5.08 BSC		
H1	6.25	6.50	6.85
L	12.75	13.50	13.80
L1	-	3.10	3.40
ΦP	3.40	3.60	3.80
Q	2.60	2.80	3.00

Version 1: TO220-C outline dimension

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	-	0.90
b1	1.27	-	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	-	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54 BSC		
e1	5.08 BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	-	-	3.50
L2	4.60 REF		
ΦP	3.55	3.60	3.65
Q	2.73	-	2.87
θ1	1°	3°	5°

Version 2: TO220-J outline dimension

Ordering Information

Package Type	Units/ Tube	Tubes / Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO220-C	50	20	1000	6	6000
TO220-J	50	20	1000	5	5000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG55R380PF	TO220	yes	yes	yes

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