

General Description

The GreenMOS® high voltage MOSFET utilizes charge balance technology to achieve outstanding low on-resistance and lower gate charge. It is engineered to minimize conduction loss, provide superior switching performance and robust avalanche capability.

The GreenMOS® Generic series is optimized for extreme switching performance to minimize switching loss. It is tailored for high power density applications to meet the highest efficiency standards.

Features

- Low $R_{DS(ON)}$ & FOM
- Extremely low switching loss
- Excellent stability and uniformity



Applications

- PC power
- LED lighting
- Telecom power
- Server power
- EV Charger
- Solar/UPS

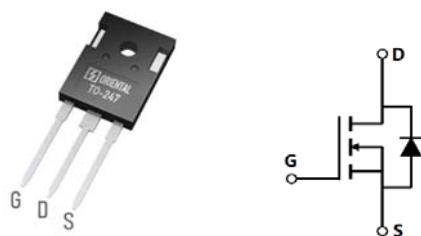
Key Performance Parameters

Parameter	Value	Unit
V_{DS} , min @ $T_{j(max)}$	600	V
I_D , pulse	120	A
$R_{DS(ON)}$, max @ $V_{GS}=10V$	92	mΩ
Q_g	38.6	nC

Marking Information

Product Name	Package	Marking
OSG55R092HF	TO247	OSG55R092H

Package & Pin Information



Absolute Maximum Ratings at $T_j=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	550	V
Gate-source voltage	V_{GS}	± 30	V
Continuous drain current ¹⁾ , $T_c=25^\circ\text{C}$	I_D	40	A
Continuous drain current ¹⁾ , $T_c=100^\circ\text{C}$		25	
Pulsed drain current ²⁾ , $T_c=25^\circ\text{C}$	$I_{D, \text{pulse}}$	120	A
Continuous diode forward current ¹⁾ , $T_c=25^\circ\text{C}$	I_S	40	A
Diode pulsed current ²⁾ , $T_c=25^\circ\text{C}$	$I_{S, \text{pulse}}$	120	A
Power dissipation ³⁾ , $T_c=25^\circ\text{C}$	P_D	101	W
Single pulsed avalanche energy ⁵⁾	E_{AS}	770	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\ldots 480\text{ V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\ldots 480\text{ V}$, $I_{SD} \leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	T_{stg}, T_j	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	1.2	°C/W
Thermal resistance, junction-ambient ⁴⁾	$R_{\theta JA}$	62	°C/W

Electrical Characteristics at $T_j=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	BV_{DSS}	550			V	$V_{GS}=0\text{ V}$, $I_D=1\text{ mA}$
		600	670			$V_{GS}=0\text{ V}$, $I_D=1\text{ mA}$, $T_j=150^\circ\text{C}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.9		3.9	V	$V_{DS}=V_{GS}$, $I_D=1\text{ mA}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		0.078	0.092	Ω	$V_{GS}=10\text{ V}$, $I_D=20\text{ A}$
			0.183			$V_{GS}=10\text{ V}$, $I_D=20\text{ A}$, $T_j=150^\circ\text{C}$
Gate-source leakage current	I_{GSS}			100	nA	$V_{GS}=30\text{ V}$
				-100		$V_{GS}=-30\text{ V}$
Drain-source leakage current	I_{DSS}			1	μA	$V_{DS}=550\text{ V}$, $V_{GS}=0\text{ V}$

Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C _{iss}		2425.5		pF	V _{GS} =0 V, V _{DS} =50 V, f=100 kHz
Output capacitance	C _{oss}		244.3		pF	
Reverse transfer capacitance	C _{rss}		10.3		pF	
Turn-on delay time	t _{d(on)}		53.7		ns	V _{GS} =10 V, V _{DS} =400 V, R _G =2 Ω, I _D =20 A
Rise time	t _r		79.3		ns	
Turn-off delay time	t _{d(off)}		122.9		ns	
Fall time	t _f		59.1		ns	

Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q _g		38.6		nC	V _{GS} =10 V, V _{DS} =400 V, I _D =20 A
Gate-source charge	Q _{gs}		9		nC	
Gate-drain charge	Q _{gd}		15.2		nC	
Gate plateau voltage	V _{plateau}		5.9		V	

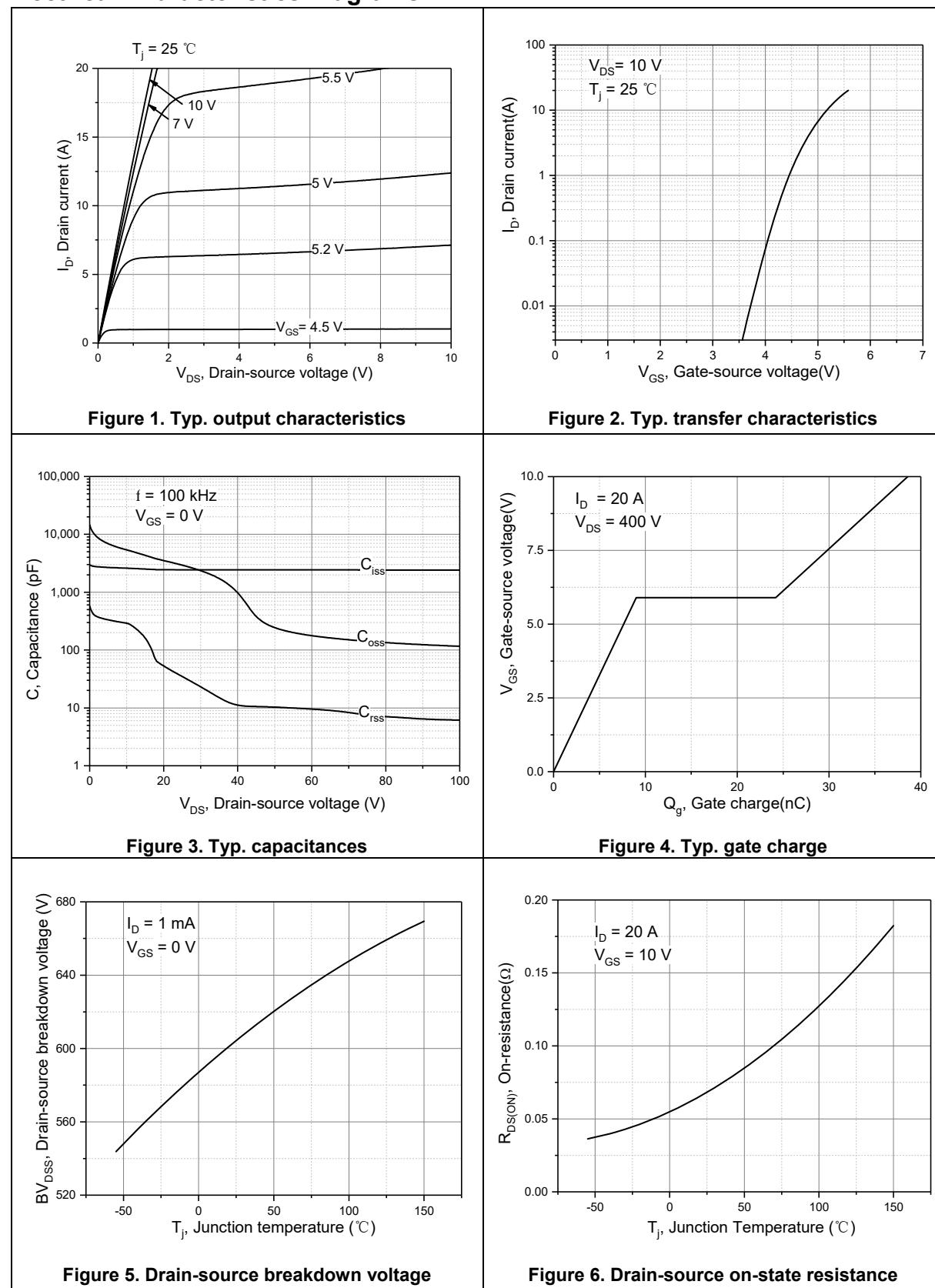
Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V _{SD}			1.4	V	I _S =40 A, V _{GS} =0 V
Reverse recovery time	t _{rr}		339		ns	V _R =400 V, I _S =20 A, di/dt=100 A/μs
Reverse recovery charge	Q _{rr}		4.9		μC	
Peak reverse recovery current	I _{rrm}		27.9		A	

Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R_{θJA} is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_a=25 °C.
- 5) V_{DD}=100 V, V_{GS}=10 V, L=10 mH, starting T_j=25 °C.

Electrical Characteristics Diagrams



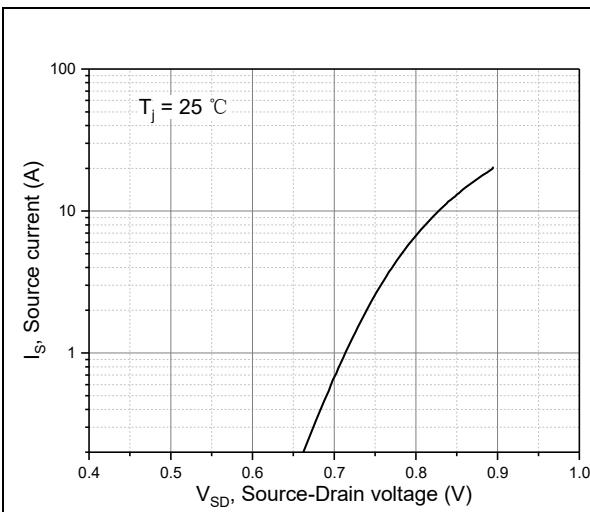


Figure 7. Forward characteristic of body diode

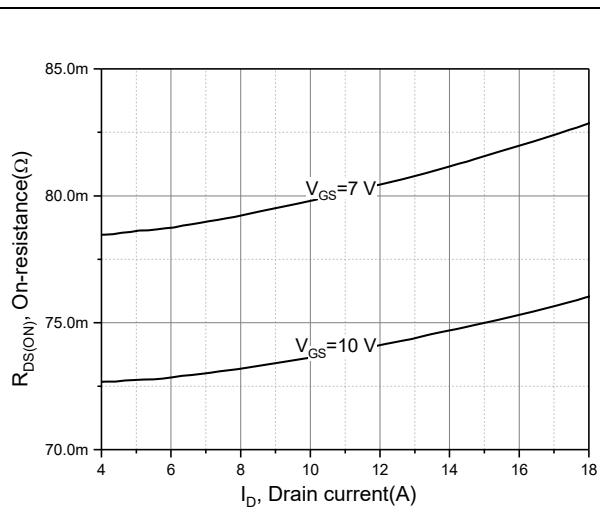


Figure 8. Drain-source on-state resistance

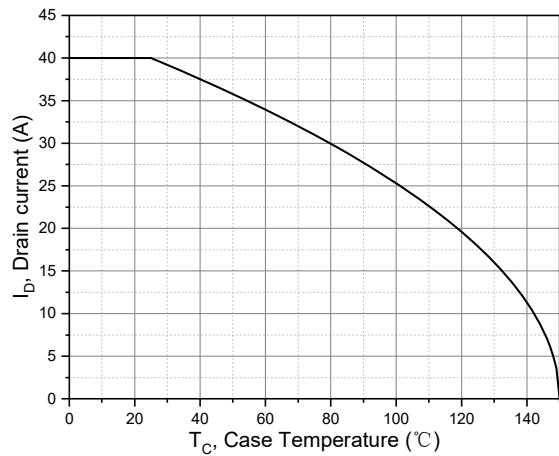


Figure 9. Drain current

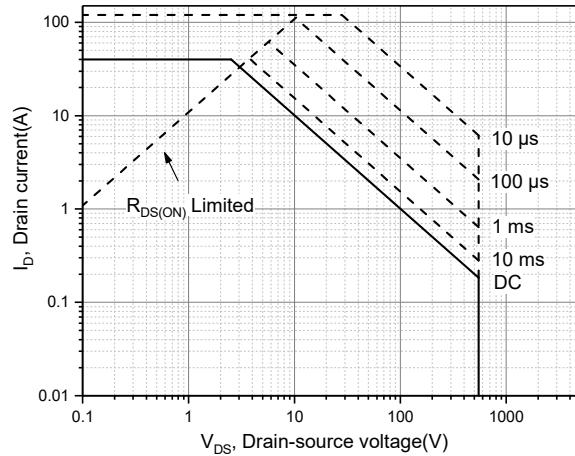


Figure 10. Safe operation area $T_c=25\text{ }^{\circ}\text{C}$

Test circuits and waveforms

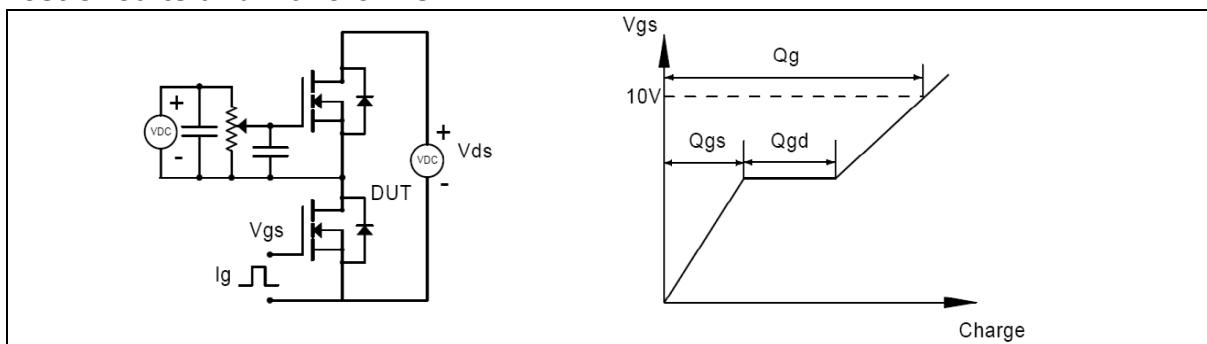


Figure 1. Gate charge test circuit & waveform

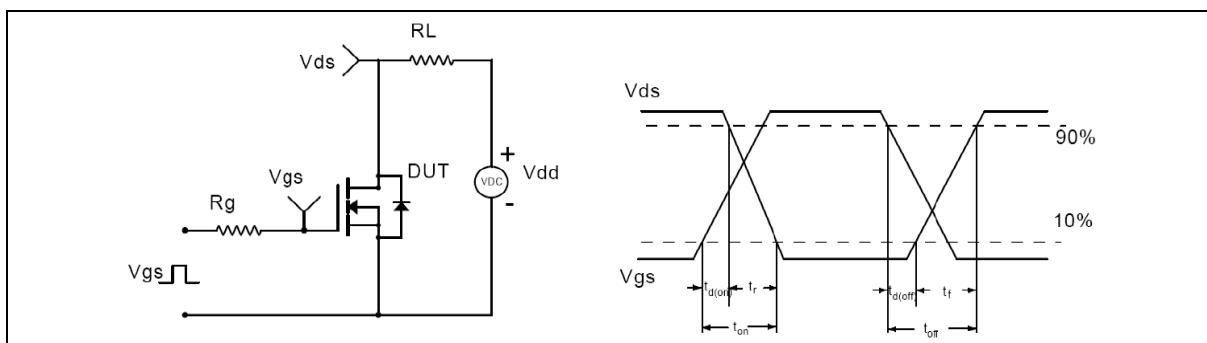


Figure 2. Switching time test circuit & waveforms

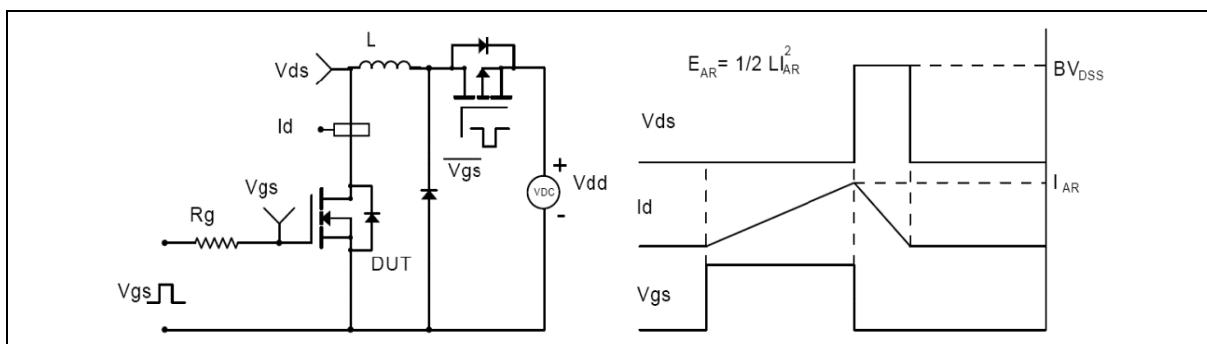


Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms

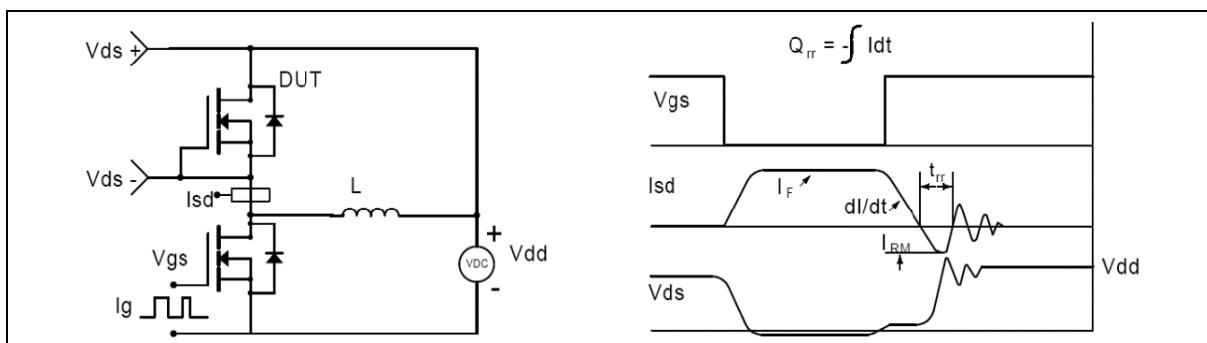
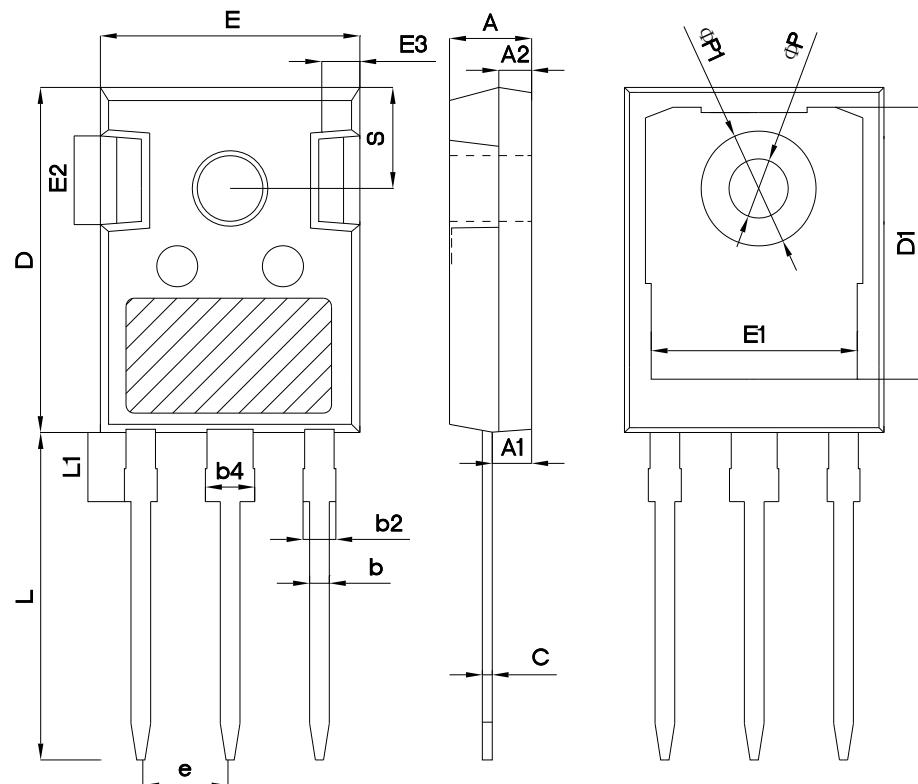


Figure 4. Diode reverse recovery test circuit & waveforms

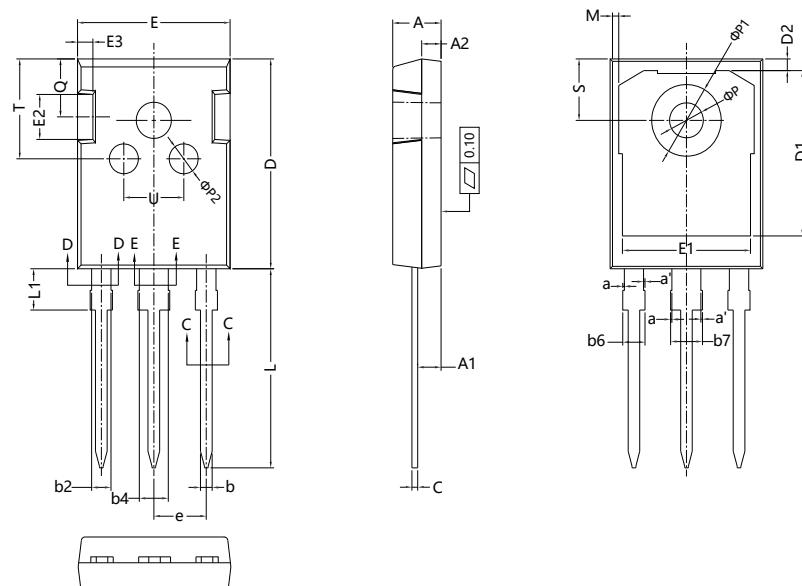
Package Information



Symbol	mm		
	Min	Nom	Max
A	4.80	5.00	5.20
A1	2.21	2.41	2.59
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.80	21.00	21.30
D1	16.25	16.55	16.85
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44 BSC		
L	19.82	19.92	20.22
L1	-	-	4.30
ΦP	3.40	3.60	3.80
ΦP1	-	-	7.30
S	6.15 BSC		

Version 1:, TO247-C package outline dimension

Package Information



Symbol	mm		
	Min	Nom	Max
A	4.90	5.00	5.20
A1	2.31	2.41	2.59
A2	1.90	2.00	2.15
a	0.00		
a'	0.00		
b	1.16	1.21	1.36
b1	1.15		
b2	1.96	2.01	2.21
b3	1.95		
b4	2.96	3.01	3.21
b5	2.96		
b6	-		
b7	-		
c	0.59	0.61	0.75
c1	0.58		
D	20.90	21.00	21.30
D1	16.25	16.55	16.85
D2	1.05		
E	15.70	15.80	16.10
E1	13.10	13.30	13.60
E2	4.40	5.00	5.20
E3	2.40	2.50	2.70
e	5.436BSC		
L	19.80	19.92	20.22
L1	-	-	4.30
M	0.35		
P	3.40	3.60	3.80
P1	7.00		
P2	2.40	-	7.30
Q	5.60		
S	6.05	-	7.30
T	0.80		
U	6.00	6.15BSC	

Version 2: TO247-J package outline dimension

Ordering Information

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO247-C	30	11	330	6	1980
TO247-J	30	20	600	5	3000

Product Information

Product	Package	Pb Free	RoHS	Halogen Free
OSG55R092HF	TO247	yes	yes	yes

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