

## General Description

SFGMOS<sup>®</sup> MOSFET is based on Oriental Semiconductor's unique device design to achieve low  $R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. The high  $V_{th}$  series is specially optimized for high systems with gate driving voltage greater than 10V.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Fast switching and soft recovery



## Applications

- Switched mode power supply
- Motor driver
- Battery protection
- DC-DC convertor
- Solar inverter
- UPS and energy inverter

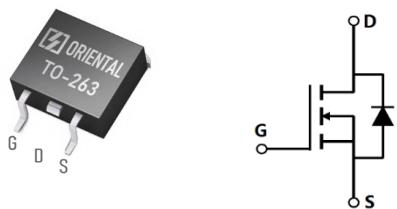
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS, min} @ T_j(max)$	80	V
$I_D, pulse$	840	A
$R_{DS(ON), max} @ V_{GS}=10V$	2.6	mΩ
$Q_g$	148.1	nC

## Marking Information

Product Name	Package	Marking
SFG280N08KF	TO263	SFG280N08K

## Package & Pin information



**Absolute Maximum Ratings** at  $T_j=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	$V_{DS}$	80	V
Gate source voltage	$V_{GS}$	$\pm 20$	V
Continuous drain current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_D$	280	A
Pulsed drain current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{D,\text{pulse}}$	840	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_S$	280	A
Diode pulsed current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{S,\text{pulse}}$	840	A
Power dissipation <sup>3)</sup> , $T_C=25^\circ\text{C}$	$P_D$	375	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	1000	mJ
Operation and storage temperature	$T_{stg}, T_j$	-55 to 150	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	0.33	$^\circ\text{C}/\text{W}$
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62	$^\circ\text{C}/\text{W}$

**Electrical Characteristics** at  $T_j=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	80			V	$V_{GS}=0 \text{ V}, I_D=250 \mu\text{A}$
Gate threshold voltage	$V_{GS(\text{th})}$	2.0		4.0	V	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		2.4	2.6	$\text{m}\Omega$	$V_{GS}=10 \text{ V}, I_D=20 \text{ A}$
Gate-source leakage current	$I_{GSS}$			100	nA	$V_{GS}=20 \text{ V}$
				-100		$V_{GS}=-20 \text{ V}$
Drain-source leakage current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=80 \text{ V}, V_{GS}=0 \text{ V}$

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C <sub>iss</sub>		10486		pF	V <sub>GS</sub> =0 V, V <sub>DS</sub> =50 V, f=1 MHz
Output capacitance	C <sub>oss</sub>		2022		pF	
Reverse transfer capacitance	C <sub>rss</sub>		19.9		pF	
Turn-on delay time	t <sub>d(on)</sub>		39.5		ns	V <sub>GS</sub> =10 V, V <sub>DS</sub> =50 V, R <sub>G</sub> =2.2 Ω, I <sub>D</sub> =25 A
Rise time	t <sub>r</sub>		27.5		ns	
Turn-off delay time	t <sub>d(off)</sub>		73.3		ns	
Fall time	t <sub>f</sub>		16.1		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q <sub>g</sub>		148.1		nC	V <sub>GS</sub> =10 V, V <sub>DS</sub> =50 V, I <sub>D</sub> =25 A
Gate-source charge	Q <sub>gs</sub>		36.5		nC	
Gate-drain charge	Q <sub>gd</sub>		35.9		nC	
Gate plateau voltage	V <sub>plateau</sub>		4.6		V	

### Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V <sub>SD</sub>			1.3	V	I <sub>S</sub> =20 A, V <sub>GS</sub> =0 V
Reverse recovery time	t <sub>rr</sub>		112		ns	V <sub>R</sub> =50 V, I <sub>S</sub> =25 A, di/dt=100 A/μs
Reverse recovery charge	Q <sub>rr</sub>		477.5		nC	
Peak reverse recovery current	I <sub>rrm</sub>		6.9		A	

### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R<sub>θJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.
- 5) V<sub>DD</sub>=50 V, V<sub>GS</sub>=10 V, L=0.3 mH, starting T<sub>j</sub>=25 °C.

### Electrical Characteristics Diagrams

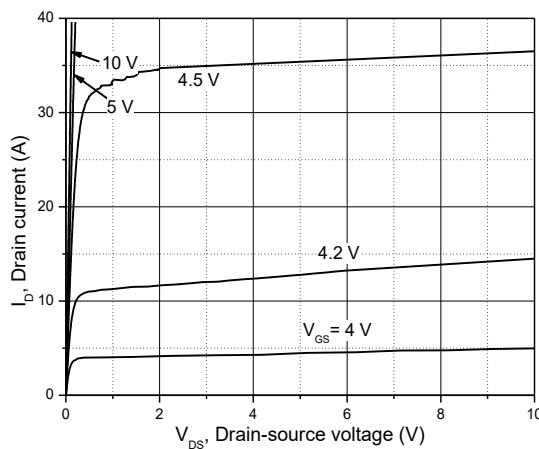


Figure 1. Typ. output characteristics

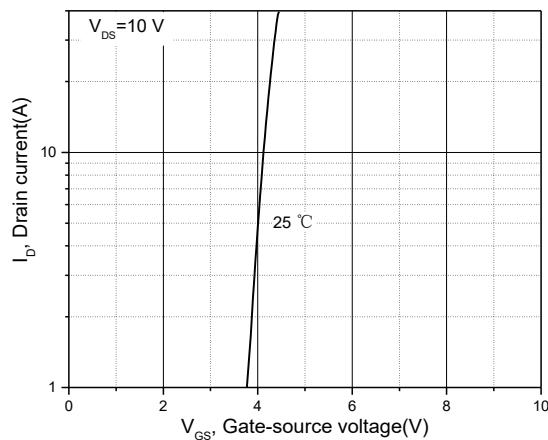


Figure 2. Typ. transfer characteristics

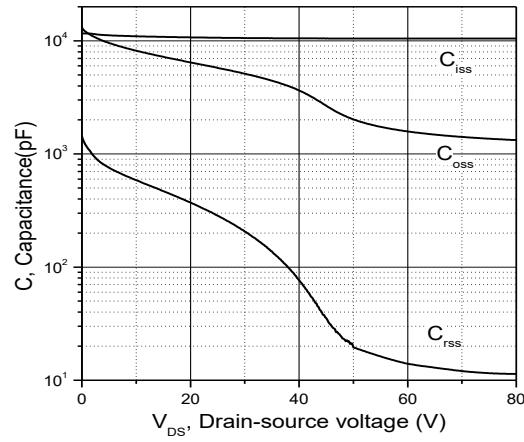


Figure 3. Typ. capacitances

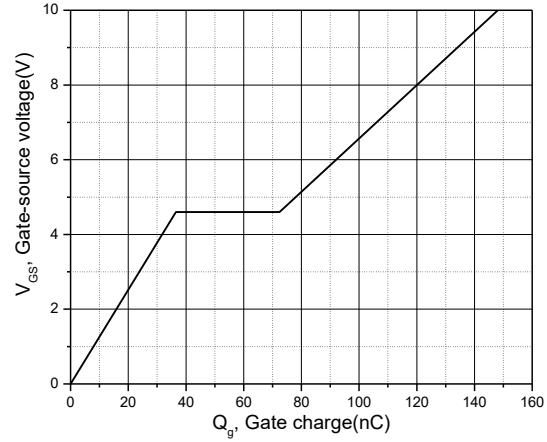


Figure 4. Typ. gate charge

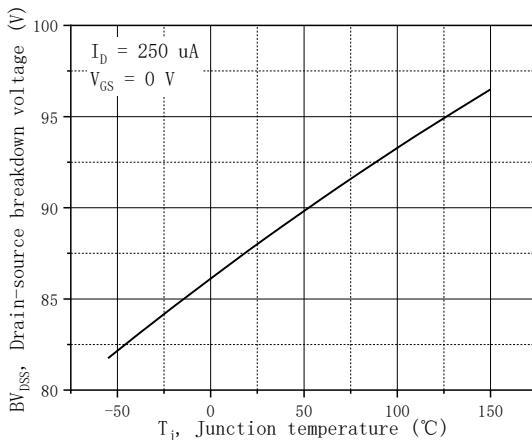


Figure 5. Drain-source breakdown voltage

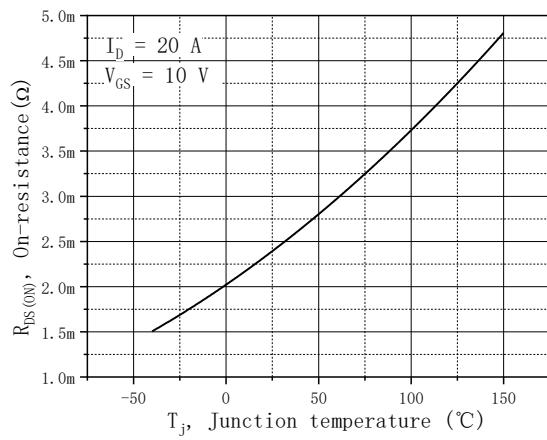
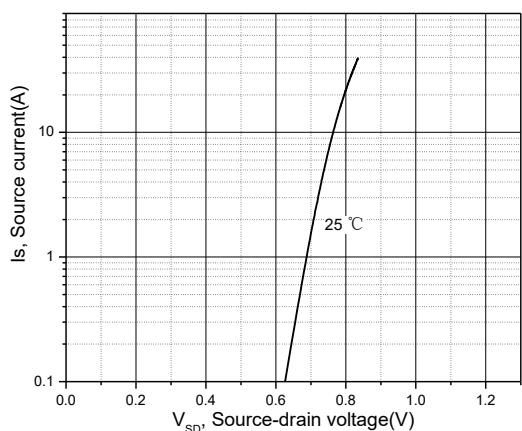
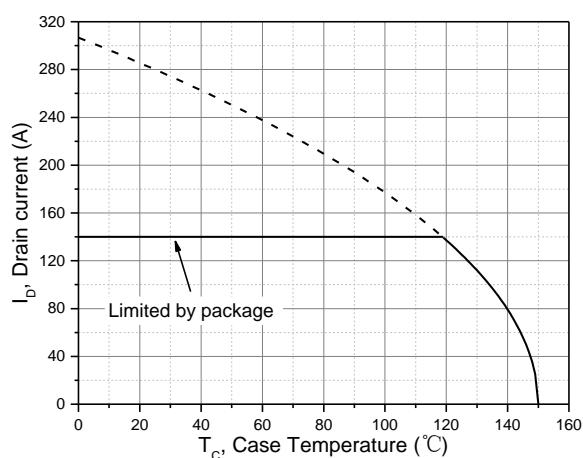


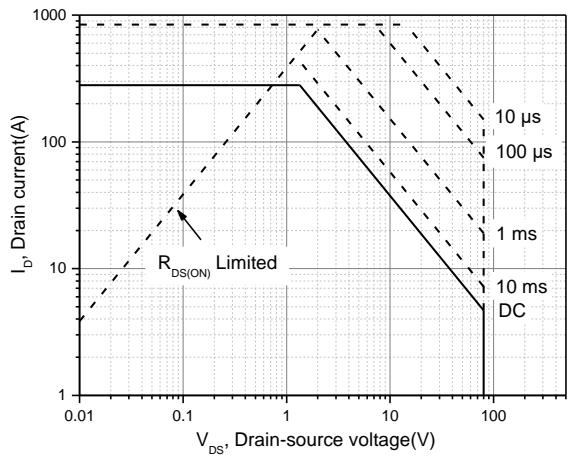
Figure 6. Drain-source on-state resistance



**Figure 7. Forward characteristic of body diode**

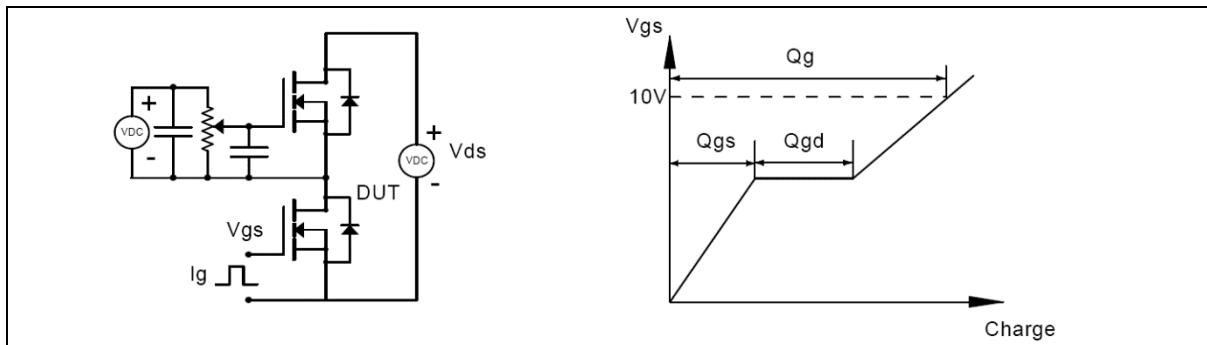


**Figure 8. Drain current**

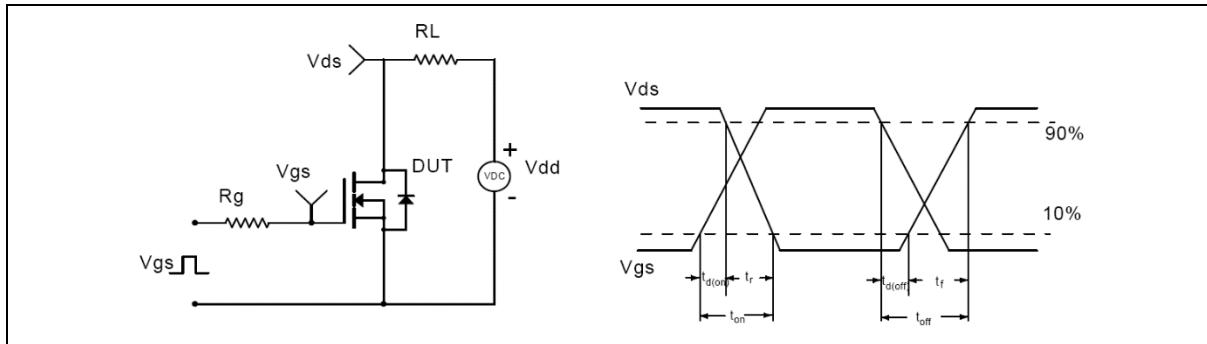


**Figure 9. Safe operation area T<sub>c</sub>=25 °C**

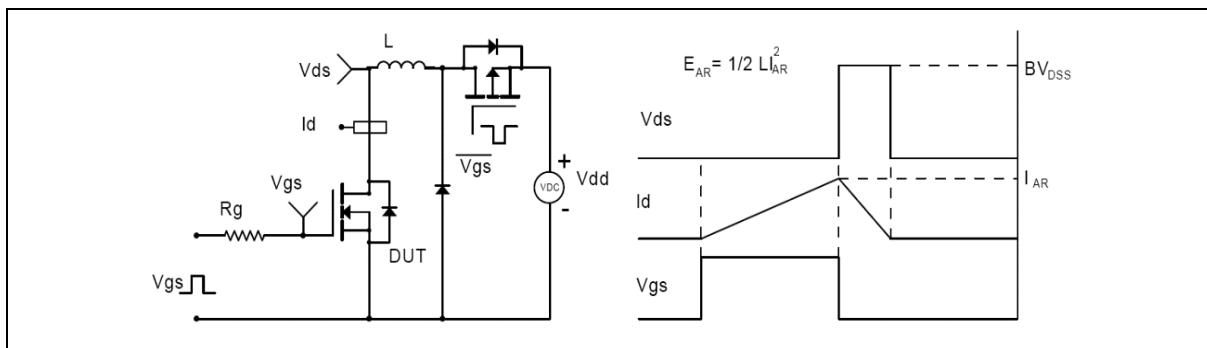
### Test circuits and waveforms



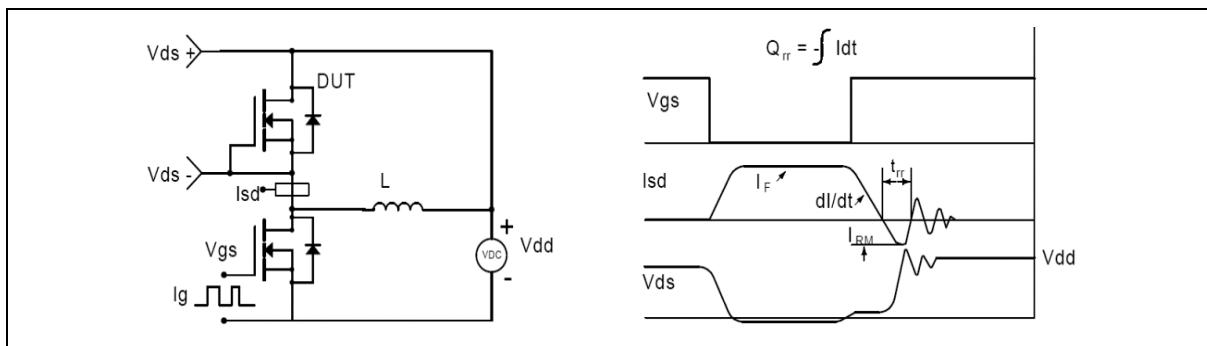
**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

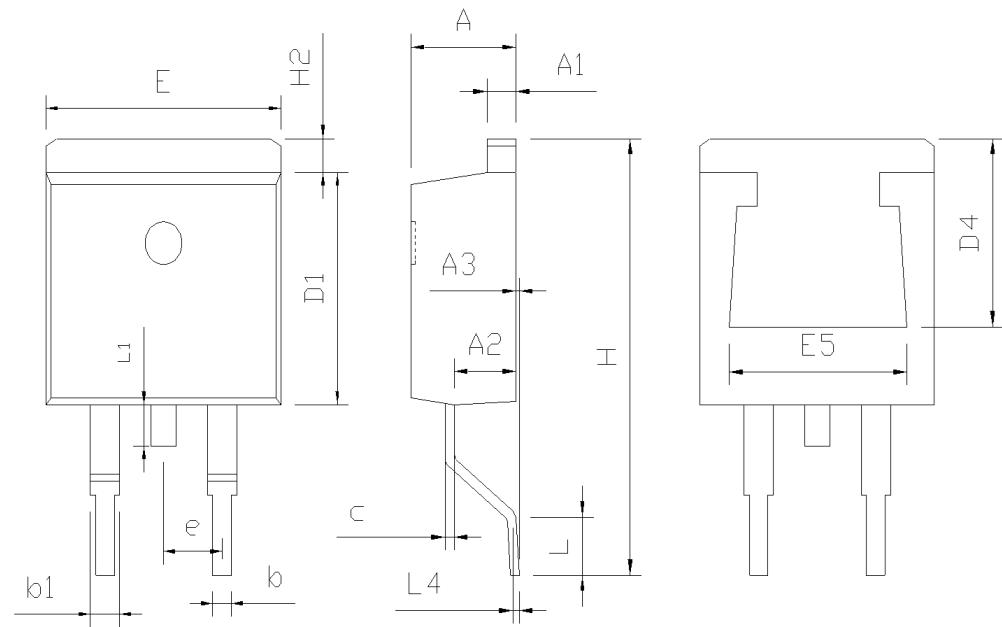


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

### Package Information



Symbol	mm		
	Min	Nom	Max
A	4.37	4.57	4.77
A1	1.22	1.27	1.42
A2	2.49	2.69	2.89
A3	0.00	0.13	0.25
b	0.70	0.81	0.96
b1	0.17	1.27	1.47
c	0.30	0.38	0.53
D1	8.50	8.70	8.90
D4	6.60	-	-
E	9.86	10.16	10.36
E5	7.06	-	-
e	2.54 BSC		
H	14.70	15.10	15.50
H2	1.07	1.27	1.47
L2	2.00	2.30	2.60
L1	1.40	1.55	1.70
L4	0.25 BSC		

Version 1: TO263-C package outline dimension

## Ordering Information

Package Type	Units/Reel	Reels / Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO263-C	800	1	800	5	4000

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFG280N08KF	TO263	yes	yes	yes

## Legal Disclaimer

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