

## General Description

FSMOS<sup>®</sup> MOSFET is based on Oriental Semiconductor's unique device design to achieve low  $R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. The low  $V_{th}$  series is specially designed to use in synchronous rectification power systems with low driving voltage.

## Features

- Low  $R_{DS(ON)}$  & FOM
- Extremely low switching loss
- Excellent reliability and uniformity
- Fast switching and soft recovery



## Applications

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC convertor
- Switched mode power supply

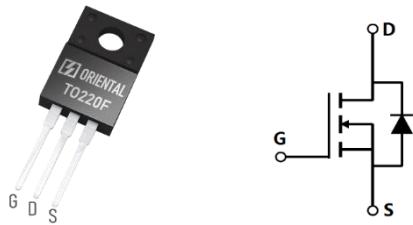
## Key Performance Parameters

Parameter	Value	Unit
$V_{DS, min} @ T_j(max)$	60	V
$I_D, pulse$	480	A
$R_{DS(ON) max} @ V_{GS}=10V$	3.5	mΩ
$Q_g$	66.1	nC

## Marking Information

Product Name	Package	Marking
SFS06R03FF	TO220F	SFS06R03F

## Package & Pin information



**Absolute Maximum Ratings** at  $T_j=25^\circ\text{C}$  unless otherwise noted

Parameter	Symbol	Value	Unit
Drain source voltage	$V_{DS}$	60	V
Gate source voltage	$V_{GS}$	$\pm 20$	V
Continuous drain current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_D$	160	A
Pulsed drain current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{D,\text{pulse}}$	480	A
Continuous diode forward current <sup>1)</sup> , $T_C=25^\circ\text{C}$	$I_S$	160	A
Diode pulsed current <sup>2)</sup> , $T_C=25^\circ\text{C}$	$I_{S,\text{Pulse}}$	480	A
Power dissipation <sup>3)</sup> , $T_C=25^\circ\text{C}$	$P_D$	33	W
Single pulsed avalanche energy <sup>5)</sup>	$E_{AS}$	200	mJ
Operation and storage temperature	$T_{stg}, T_j$	-55 to 175	$^\circ\text{C}$

**Thermal Characteristics**

Parameter	Symbol	Value	Unit
Thermal resistance, junction-case	$R_{\theta JC}$	4.5	$^\circ\text{C/W}$
Thermal resistance, junction-ambient <sup>4)</sup>	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$

**Electrical Characteristics** at  $T_j=25^\circ\text{C}$  unless otherwise specified

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Drain-source breakdown voltage	$BV_{DSS}$	60			V	$V_{GS}=0 \text{ V}, I_D=250 \mu\text{A}$
Gate threshold voltage	$V_{GS(\text{th})}$	1.3		2.5	V	$V_{DS}=V_{GS}, I_D=250 \mu\text{A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		3.0	3.5	$\text{m}\Omega$	$V_{GS}=10 \text{ V}, I_D=20 \text{ A}$
Drain-source on-state resistance	$R_{DS(\text{ON})}$		3.5	4.5	$\text{m}\Omega$	$V_{GS}=4.5 \text{ V}, I_D=10 \text{ A}$
Gate-source leakage current	$I_{GSS}$			100	$\text{nA}$	$V_{GS}=20 \text{ V}$
				-100		$V_{GS}=-20 \text{ V}$
Drain-source leakage current	$I_{DSS}$			1	$\mu\text{A}$	$V_{DS}=60 \text{ V}, V_{GS}=0 \text{ V}$

### Dynamic Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Input capacitance	C <sub>iss</sub>		5377		pF	V <sub>GS</sub> =0 V, V <sub>DS</sub> =25 V, f=100 kHz
Output capacitance	C <sub>oss</sub>		1666		pF	
Reverse transfer capacitance	C <sub>rss</sub>		77.7		pF	
Turn-on delay time	t <sub>d(on)</sub>		22.5		ns	V <sub>GS</sub> =10 V, V <sub>DS</sub> =30 V, R <sub>G</sub> =2 Ω, I <sub>D</sub> =25 A
Rise time	t <sub>r</sub>		6.7		ns	
Turn-off delay time	t <sub>d(off)</sub>		80.3		ns	
Fall time	t <sub>f</sub>		26.8		ns	

### Gate Charge Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Total gate charge	Q <sub>g</sub>		66.1		nC	V <sub>GS</sub> =10 V, V <sub>DS</sub> =30 V, I <sub>D</sub> =25 A
Gate-source charge	Q <sub>gs</sub>		10.7		nC	
Gate-drain charge	Q <sub>gd</sub>		10.9		nC	
Gate plateau voltage	V <sub>plateau</sub>		2.9		V	

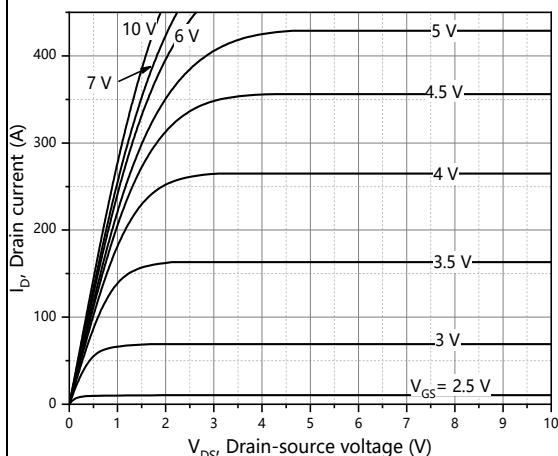
### Body Diode Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test condition
Diode forward voltage	V <sub>SD</sub>			1.3	V	I <sub>S</sub> =20 A, V <sub>GS</sub> =0 V
Reverse recovery time	t <sub>rr</sub>		68.3		ns	V <sub>R</sub> =30 V, I <sub>S</sub> =25 A, di/dt=100 A/μs
Reverse recovery charge	Q <sub>rr</sub>		73.0		nC	
Peak reverse recovery current	I <sub>rrm</sub>		1.9		A	

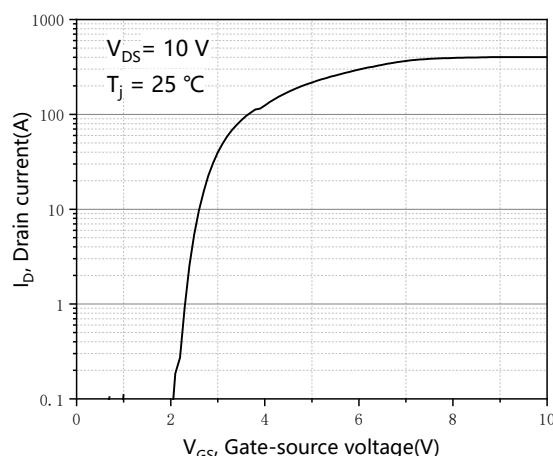
### Note

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) Pd is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of R<sub>θJA</sub> is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T<sub>a</sub>=25 °C.
- 5) V<sub>DD</sub>=30 V, V<sub>GS</sub>=10 V, L=0.3 mH, starting T<sub>j</sub>=25 °C.

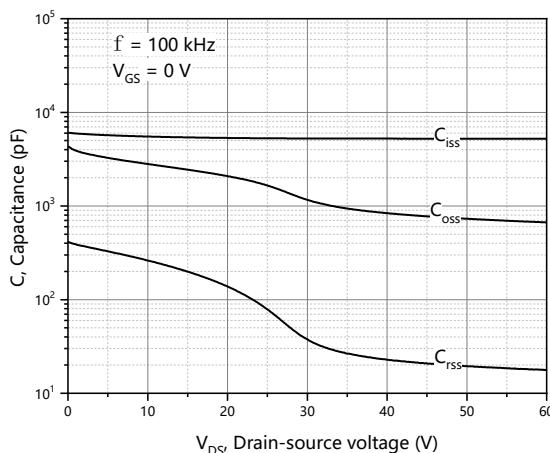
## Electrical Characteristics Diagrams



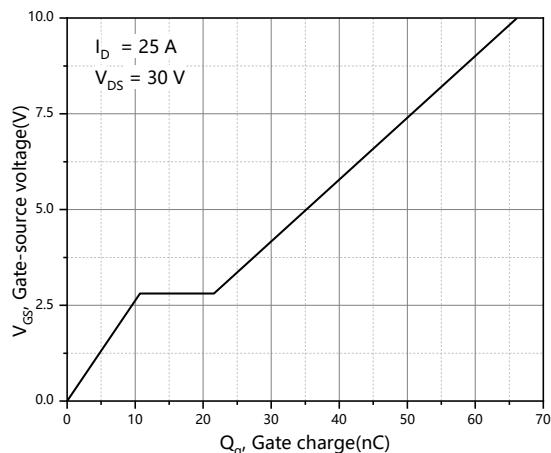
**Figure 1. Typ. output characteristics**



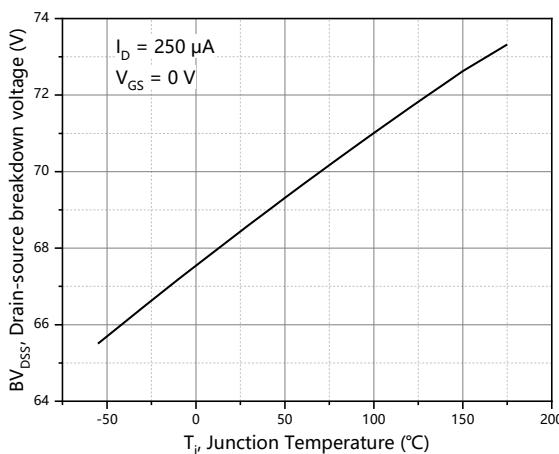
**Figure 2. Typ. transfer characteristics**



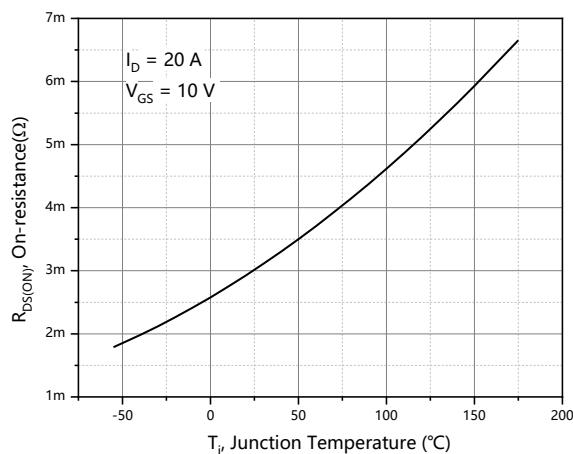
**Figure 3. Typ. capacitances**



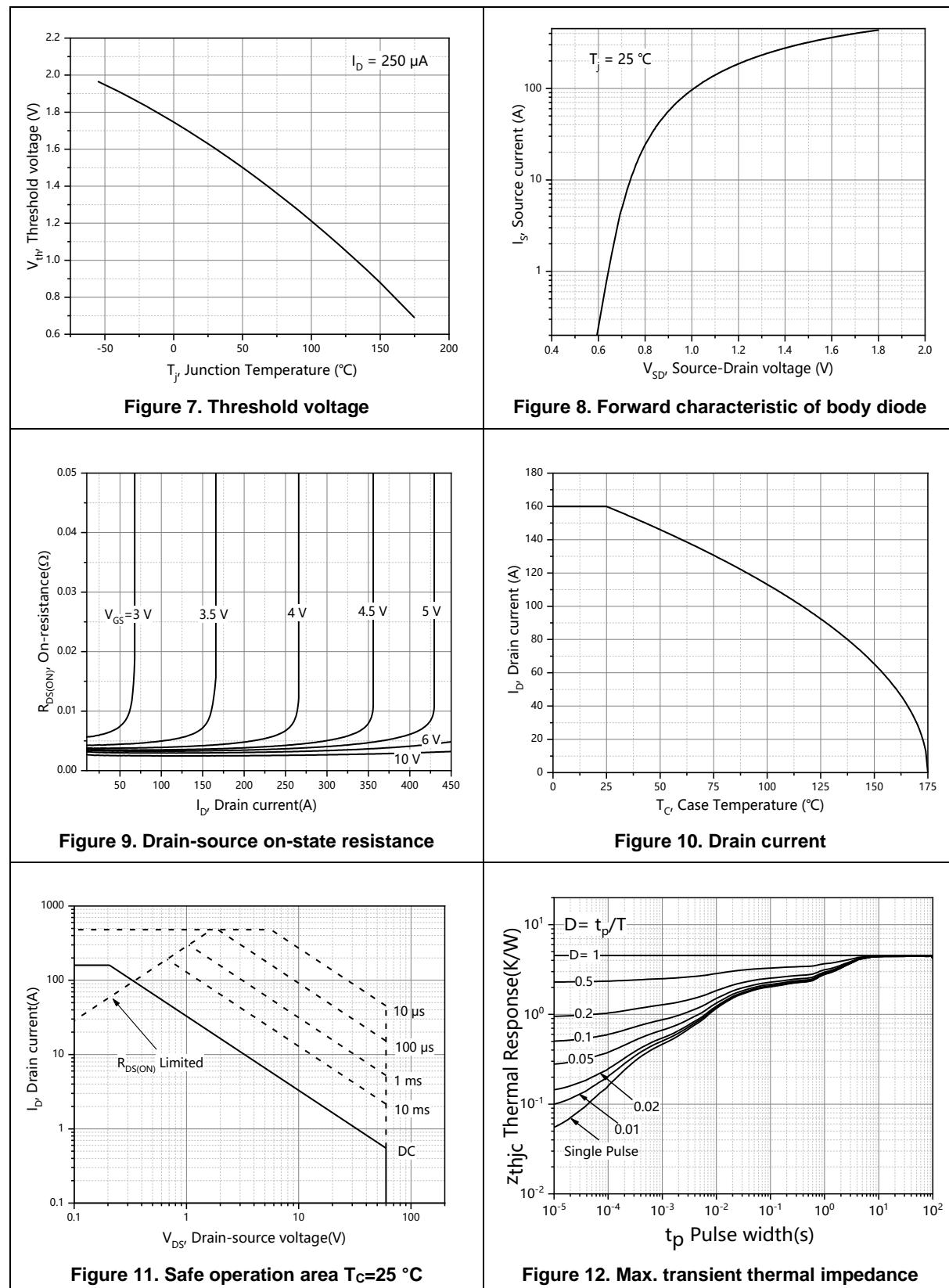
**Figure 4. Typ. gate charge**



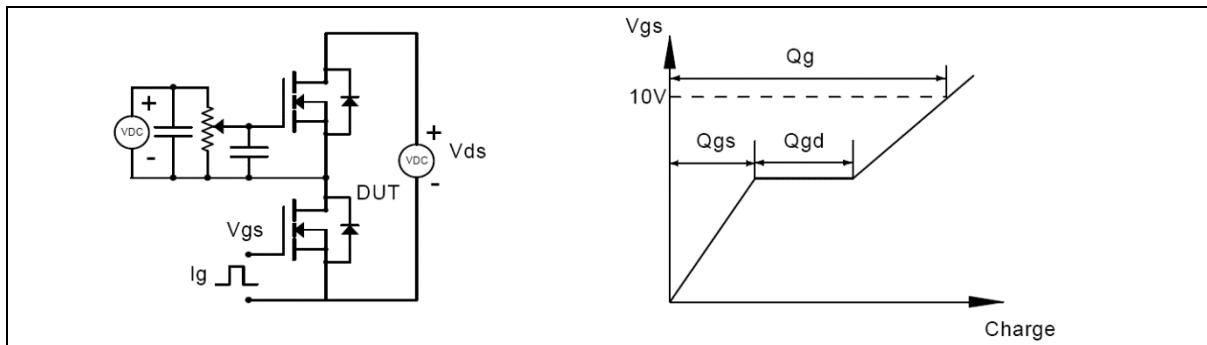
**Figure 5. Drain-source breakdown voltage**



**Figure 6. Drain-source on-state resistance**



### Test circuits and waveforms



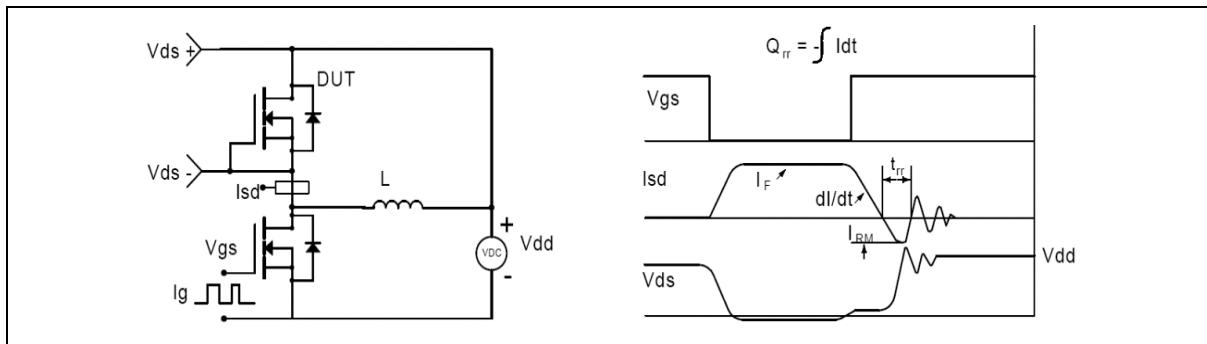
**Figure 1. Gate charge test circuit & waveform**



**Figure 2. Switching time test circuit & waveforms**

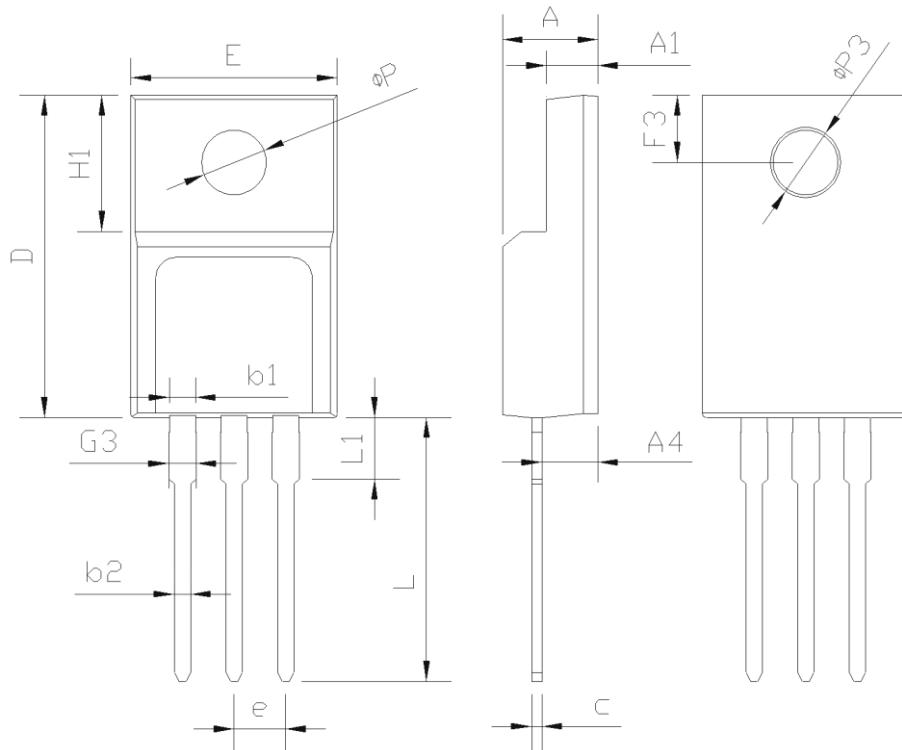


**Figure 3. Unclamped inductive switching (UIS) test circuit & waveforms**



**Figure 4. Diode reverse recovery test circuit & waveforms**

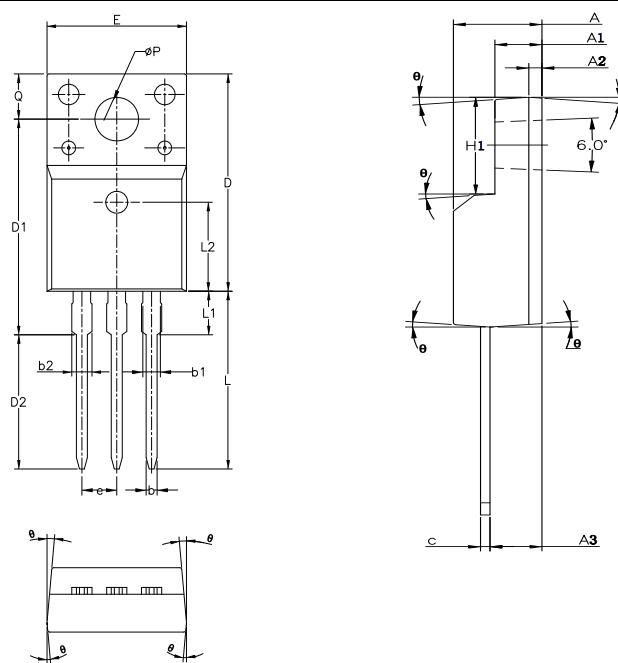
### Package Information



Symbol	mm		
	Min	Nom	Max
E	9.96	10.16	10.36
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A4	2.56	2.76	2.96
c	0.40	0.50	0.65
D	15.57	15.87	16.17
H1	6.70 REF		
e	2.54 BSC		
L	12.68	12.98	13.28
L1	2.88	3.03	3.18
ΦP	3.03	3.18	3.38
ΦP3	3.15	3.45	3.65
F3	3.15	3.30	3.45
G3	1.25	1.35	1.55
b1	1.18	1.28	1.43
b2	0.70	0.80	0.95

Version 1: TO220F-C package outline dimension

### Package Information



Symbol	mm		
	Min	Nom	Max
A	4.50	4.70	4.83
A1	2.34	2.54	2.74
A2	0.70 REF		
A3	2.56	2.76	2.93
b	0.70	-	0.90
b1	1.18	-	1.38
b2	-	-	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	9.60	9.80	10.00
E	9.96	10.16	10.36
e	2.54 BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	-	-	3.50
L2	6.50 REF		
ΦP	3.08	3.18	3.28
Q	3.20	-	3.40
θ	1°	3°	5°

Version 2: TO220F-J package outline dimension



## Ordering Information

Package Type	Units/Tube	Tubes / Inner Box	Units/ Inner Box	Inner Boxes/ Carton Box	Units/ Carton Box
TO220F-C	50	20	1000	6	6000
TO220F-J	50	20	1000	5	5000

## Product Information

Product	Package	Pb Free	RoHS	Halogen Free
SFS06R03FF	TO220F	yes	yes	yes

## Legal Disclaimer

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics. With respect to any examples or hints given herein, any typical values stated herein and/or any information regarding the application of the device, Oriental Semiconductor hereby disclaims any and all warranties and liabilities of any kind, including without limitation, warranties of non-infringement of intellectual property rights of any third party.

For further information on technology, delivery terms and conditions and prices, please contact the Oriental Semiconductor sales representatives ([www.orientalsemi.com](http://www.orientalsemi.com)).

© Oriental Semiconductor Co.,Ltd. All Rights Reserved

